



Product Change Notification

108231 - 01

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Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

Americas Contact: asmo.pcn@intel.com

Asia Pacific Contact: apacgccb@intel.com

Europe Email: eccb@intel.com

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Product Change Notification

Change Notification #: 108231 - 01

Change Title: Intel® Core™2 Extreme Processor QX9650, PCN 108231-01, Product Design, C-0 to C-1 Stepping Conversion, Reason for Revision: Adjust the Customer Ready to Receive Date

Date of Publication: April 02, 2008

Key Characteristics of the Change:

Product Design

Forecasted Key Milestones:

Date of Samples Availability:	Feb 15, 2008
Date of Qualification Data Availability:	Mar 03, 2008
Date Customer Must be Ready to Receive Post-Conversion Material:	Aug 4, 2008
Date of First Availability of Post-Conversion Material:	Mar 03, 2008

The date of "First Availability of Post-Conversion Material" is the projected date that a customer may expect to receive the Post-Conversion Materials. This date is determined by the projected depletion of inventory at the time of the PCN publication. The depletion of inventory may be impacted by fluctuating supply and demand, therefore, although customers should be prepared to receive the Post-Converted Materials on this date, Intel will continue to ship and customers may continue to receive the pre-converted materials until the inventory has been depleted.

Description of Change to the Customer:

Reason for Revision: Adjust the Customer Ready to Receive Date to August 4, 2008.

The Intel® Core™2 Extreme Processor QX9650 will undergo the following changes for the C-0 to C-1 stepping conversion:

- New SSPEC and MM numbers for the converting product
- CPUID will change from: 0x00010676 to: 0x00010677
- C-0 package is pin compatible with C-1 package
- There are no changes to Electrical, Mechanical and Thermal processor Specifications. Intel anticipates no changes to customer platforms designed to Intel guidelines.

Customer Impact of Change and Recommended Action:

Minimal re-qualification and/or validation is expected for the stepping conversion due to no feature set changes between the C-0 and C-1 steppings.

The Intel® Core™2 Extreme Processor QX9650 C-1 stepping will require a BIOS update. Once customers implement the BIOS update, they will be able to accept both C-0 stepping material and C-1 stepping material. Customers should be ready to receive a combination of both C-0 stepping material and C-1 stepping material by the “Date Customer Must be Ready to Receive Post-Conversion Material” above. For customers implementing their own BIOS, it is necessary to incorporate the BIOS changes to support C-1 stepping. Please contact your local Intel representative for further technical questions.

Products Affected / Intel Ordering Codes:

Processor#	Frequency	Pre Conversion Product Code	Pre Conversion S-Spec	Pre Conversion MM#	Post Conversion Qualification Samples QDF#	Post Conversion Qualification Samples MM#	Post Conversion Product Code	Post Conversion S-Spec	Post Conversion MM#
QX9650	3 GHz	EU80569XJ080NL	SLAN3	893496	QEFX	896320	EU80569XJ080NL	SLAWN	896591

Reference Documents / Attachments:

Document:

Location #:

PCN Revision History:

Date of Revision:

January 21, 2008

April 02, 2008

Revision Number:

00

01

Reason:

Originally Published PCN

Adjust the Customer Ready to Receive Date