Product Change Notification

112101 - 00

Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

Americas Contact: asmo.pcn@intel.com

Asia Pacific/PRC Contact: apacgccb@intel.com

Europe Email: eccb@intel.com

Japan Email: jccb.ijkk@intel.com

Product Change Notification

Change Notification #:   112101-00

Change Title: **Intel® 8 Series/C220 Series, PCN 112101-00, Product Design, C1 to C2 Stepping Conversion**

Date of Publication: **April 04, 2013**

Key Characteristics of the Change:

Product Design

Forecasted Key Milestones:

Date of Samples Availability	April 19, 2013
Date of Qualification Data Availability	July 1, 2013
Date of First Availability of Post-Conversion Material	July 15, 2013
Date Customer Must be Ready to Receive Post-Conversion Material:	July 31, 2013

The date of "First Availability of Post-Conversion Material" is the projected date that a customer may expect to receive the new Materials. This date is determined by the projected depletion of inventory at the time of the PCN publication. The depletion of inventory may be impacted by fluctuating supply and demand, therefore, although customers should be prepared to receive the Post-Converted Materials on this date, Intel will continue to ship and customers may continue to receive the pre-converted materials until the inventory has been depleted.

Description of Change to the Customer:

Intel is initiating a C1 to C2 stepping conversion for the Intel® 8 Series/C220 Series where products will undergo the following changes:

- Metal layers change from C1 to C2 to fix USB SuperSpeed device remuneration erratum.
- C1 stepping package is pin compatible with C2 stepping package.
- Revision ID will change from 04h to 05h.
- Compatibility revision ID will remain at 04h for Intel® Stable Image Platform Program (Intel® SIPP).
- New S-specs and MM numbers for the converting products.

Customer Impact of Change and Recommended Action:

C2 stepping has a metal layers change to fix the USB SuperSpeed device remuneration erratum. Based on USB SuperSpeed design fix implementation there is no need for full electrical regression testing. Customers may perform a limited USB SuperSpeed regression by checking a small subset of bulk, periodic and isochronous device types against customer's existing USB3 validation tests focusing on S3/U3 cycles.

No requalification and/or validation are expected for features already supported on C1 stepping.

The C2 stepping of the Intel® 8 Series/ C220 Series Chipset Family is pin-to-pin compatible with the C1 stepping and does not require any motherboard modifications.

Customers who want to continue using the Intel® Stable Image Platform Program (Intel® SIPP) drivers with C2 stepping should confirm BIOS support for Compatibility Rev ID (CRID).

Customers should be ready to receive a combination of C1 stepping material and C2 stepping material by the "Date Customer Must be Ready to Receive Post-Conversion Material" as shown in the milestones above.

A customer may request an extension to receive pre-conversion material after the Ready-To-Receive (RTR) date contained in this Product Change Notification. Provided Intel can support the volume and timing of the extension request, all confirmed extension support must be booked by the customer within 7 days of receiving confirmation or the volume will be made available to support other requests. All requests are supported on a first come, first serve basis. Unless otherwise agreed to in writing by Intel, all material booked on orders corresponding to the approved extension requests beyond RTR date are Non-Cancellable, Non-returnable (NCNR). Please contact your local Intel representative for product availability.

Products Affected / Intel Ordering Codes:

Marketing Name	Platform	Pre-Conversion Material				Post Conversion Material			
		Product Code	S-Spec	MM#	Stepping	Product Code	S-Spec	MM#	Stepping
Intel® DH82Q87 Platform Controller Hub	Desktop	DH82Q87	SR137	927355	C1	DH82Q87	SR173	929132	C2
Intel® DH82Q85 Platform Controller Hub	Desktop	DH82Q85	SR138	927356	C1	DH82Q85	SR174	929133	C2
Intel® DH82H87 Platform Controller Hub	Desktop	DH82H87	SR139	927357	C1	DH82H87	SR175	929134	C2
Intel® DH82Z87 Platform Controller Hub	Desktop	DH82Z87	SR13A	927358	C1	DH82Z87	SR176	929136	C2
Intel® DH82B85 Platform Controller Hub	Desktop	DH82B85	SR13C	927360	C1	DH82B85	SR178	929138	C2
Intel® DH82C222 Platform Controller Hub	Server	DH82C222	SR13F	927363	C1	DH82C222	SR17B	929144	C2
Intel® DH82C224 Platform Controller Hub	Server	DH82C224	SR13E	927362	C1	DH82C224	SR17A	929142	C2
Intel® DH82C226 Platform Controller Hub	Workstation Server	DH82C226	SR13D	927361	C1	DH82C226	SR179	929139	C2
Intel® DH82QM87 Platform Controller Hub	Mobile	DH82QM87	SR13G	927364	C1	DH82QM87	SR17C	929149	C2
Intel® DH82HM87 Platform Controller Hub	Mobile	DH82HM87	SR13H	927365	C1	DH82HM87	SR17D	929151	C2
Intel® DH82HM86 Platform Controller Hub	Mobile	DH82HM86	SR13J	927366	C1	DH82HM86	SR17E	929152	C2

PCN Revision History:

Date of Revision:	Revision Number:	Reason:
April 4, 2013	00	Originally Published PCN