



Product Change Notification

105815 - 00

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Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

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Product Change Notification

Change Notification #: 105815 - 00
Change Title: Boxed Intel® Celeron® D Processors with 533 FSB on 90 nm Process Technology in the 478 Pin Package, PCN 105815-00, FYI, E-0 to G-1 core stepping conversion
Date of Publication: January 11, 2006

Type of Change Notification:
FYI - (For Your Information)

Key Characteristics of the Change:
Product Design

Forecasted Key Milestones:

Date Customer Must be Ready to Receive Post-Conversion Material:	Feb 17, 2006
Date of First Availability of Post-Conversion Material:	Feb 17, 2006

The date of "First Availability of Post-Conversion Material" is the projected date that a customer may expect to receive the Post-Conversion Materials. This date is determined by the projected depletion of inventory at the time of the PCN publication. The depletion of inventory may be impacted by fluctuating supply and demand, therefore, although customers should be prepared to receive the Post-Converted Materials on this date, Intel will continue to ship and customers may continue to receive the pre-converted materials until the inventory has been depleted.

Description of Change to the Customer:

Desktop Intel® Celeron® processors with 533 FSB on 90 nm Process Technology in the 478 pin package will undergo the following changes to the G-1 core processor stepping:

- The CPU ID will change from 0XF41 to 0XF49 (Updated Bios required)
- No impact anticipated to existing platforms 845 and 865 family
- New S-Specs for affected product line items
- No change to Electrical and Mechanical Specification
- Desktop G-1 Step processor is Pb-Free Second Level Interconnect and will be marked with e4 designation on the integrated heat spreader (IHS) per JESD97 guidelines
- G-1 is pin compatible with E-0. The thermal/mechanical specification is within overall package height specification
- Intel anticipates no impact to current platforms designed to Intel guidelines
- Additional resistor added to the package

Please refer to the latest version of the NDA Spec Update for specification information.

Customer Impact of Change and Recommended Action:

N/A

Products Affected / Intel Ordering Codes:

Component Product Table

Pre-Conversion Production Units					Post-Conversion Production Units			
Frequency	Production Codes	Processor Number	S-Spec	MM #	Production Codes	Processor Number	S-Spec	MM #
3.20 GHz	BX80546RE3200C	350	SL7NY	868820	BX80546RE3200C	350	SL8HQ	873175
3.06 GHz	BX80546RE3066C	345	SL7NX	868133	BX80546RE3066C	345	SL8HP	873173
2.93 GHz	BX80546RE2933C	340	SL7TS	868132	BX80546RE2933C	340	SL8HN	873174
2.80 GHz	BX80546RE2800C	335	SL7NW	868131	BX80546RE2800C	335	SL8HM	873171
2.66 GHz	BX80546RE2667C	330	SL7NV	868129	BX80546RE2667C	330	SL8HL	873170
2.53 GHz	BX80546RE2533C	325	SL7NU	868128	BX80546RE2533C	325	SL8HK	873166
2.40 GHz	BX80546RE2400C	320	SL87J	868126	BX80546RE2400C	320	SL8HJ	873165
2.26 GHz	BX80546RE2267C	315	SL87K	868818	BX80546RE2267C	315	SL8HH	873162
2.13 GHz	BX80546RE2130C	310	SL8RZ	874311	BX80546RE2130C	310	SL8S2	874307

Reference Documents / Attachments:

Document:

Location #:

PCN Revision History:

Date of Revision:

January 11, 2006

Revision Number:

00

Reason:

Originally Published PCN