



# Product Change Notification

## 107687 - 00

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Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

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# Product Change Notification

**Change Notification #:** 107687 - 00  
**Change Title:** Boxed Intel® Core™2 Quad Processor Q6600 and Intel® Xeon® Processors X3220 and X3210, PCN 107687-00, Product Design, B-3 to G-0 Stepping Conversion  
**Date of Publication:** June 27, 2007

## Key Characteristics of the Change:

Product Design

## Forecasted Key Milestones:

<b>Date Customer Must be Ready to Receive Post-Conversion Material:</b>	Jul 27, 2007
<b>Date of First Availability of Post-Conversion Material:</b>	Jul 27, 2007

*The date of "First Availability of Post-Conversion Material" is the projected date that a customer may expect to receive the Post-Conversion Materials. This date is determined by the projected depletion of inventory at the time of the PCN publication. The depletion of inventory may be impacted by fluctuating supply and demand, therefore, although customers should be prepared to receive the Post-Converted Materials on this date, Intel will continue to ship and customers may continue to receive the pre-converted materials until the inventory has been depleted.*

## Description of Change to the Customer:

The boxed Intel® Core™2 Quad processor Q6600 and boxed Intel® Xeon® processors X3220 and X3210 will undergo the following changes for the B-3 to G-0 processor stepping conversion:

- CPUID will change from 06F7 to 06FB
- New S-specs for converting products
- The converting products will change from the 2005 Performance FMB(105W) to the 2005 Mainstream FMB (95W)
- The Electrical and Mechanical Specifications remain within the current specifications. Intel anticipates no changes to customer platforms designed to previous Intel guidelines.
- Thermal specification has changed: Maximum allowed Case Temperature for the Intel® Core™2 Quad processor Q6600 and Intel® Xeon® processors X3220 and X3210 on G-0 stepping has been increased by 11 °C. Tcontrol offset will remain same relative to 0 with increase in Maximum allowed Case Temperature, which will help reduce acoustics.

## Customer Impact of Change and Recommended Action:

Minimal re-qualification and/or validation is expected for the G-0 stepping conversion due to no feature set changes between the B-3 and G-0 steppings. The boxed Intel® Core™2 Quad processors Q6600 and Intel® Xeon® processors X3220 and X3210 G-0 stepping will require a BIOS update.

- Refer to Intel® Core™2 Extreme Processor QX6000 Sequence and Intel® Core™2 Quad Processor Q6000 Sequence Electrical, Mechanical, and Thermal Specifications (EMTS)
- Refer to Quad-Core Intel® Xeon® Processor 3200 Series EMTS.

## Products Affected / Intel Ordering Codes:

Processor Number	Frequency	Pre Conversion			Post Conversion		
		Product Code	S-Spec	MM#	Product Code	S-Spec	MM#
Q6600	2.40 GHz	BX80562Q6600	SL9UM	887620	BX80562Q6600	SLACR	891337
X3220	2.40 GHz	BX80562X3220	SL9UP	886869	BX80562X3220	SLACT	891336
X3210	2.13 GHz	BX80562X3210	SL9UQ	886867	BX80562X3210	SLACU	891335

## Reference Documents / Attachments:

Document:

Location #:

## PCN Revision History:

Date of Revision:

Revision Number:

Reason:

June 27, 2007

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Originally Published PCN