



Product Change Notification

Change Notification #: 118144 - 00
Change Title: Intel® Arria® 10 and Intel® Cyclone® 10 Devices, PCN 118144-00, Software, Software Update
Date of Publication: March 15, 2021

Key Characteristics of the Change:

Software

Forecasted Key Milestones:

Availability of Software Update	Now
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Description of Change to the Customer:

This is the same change described in ADV2103 issued on March 5, 2021.

Intel is notifying customers of important software updates to the Intel® Arria® 10 Devices and Intel® Cyclone® 10 Devices. Refer to table 1 and table 2 for further details and links to KDB articles with recommendations and further actions.

Table 1: Intel® Quartus® Prime Pro Software Fixes

Update Details	Impacted Versions	KDB Article (with patches)
Physical synthesis compiler fix (1) <ul style="list-style-type: none">Seed dependent functional failures in timing clean designs when compiled in impacted versions of the software.Permanent fix implemented in Intel® Quartus® Prime Pro ver 20.2 and above.	Ver. 18.1 Ver 19.1 Ver 19.2 Ver 19.3 Ver 19.4 Ver 20.1	Why are there seed dependent functional failures in a timing clean design when compiled in the Intel® Quartus® Prime Pro Edition software version 20.1 and earlier?
Physical synthesis compiler fix (2) <ul style="list-style-type: none">Seed dependent RAM data corruption when compiled in impacted versions of the software.Permanent fix implemented in Intel Quartus Prime Pro ver 20.2 and above.	Ver 18.1 Ver 19.1 Ver 19.2 Ver 19.3 Ver 19.4 Ver 20.1	Why is there seed dependent RAM data corruption in designs compiled in the Intel® Quartus® Prime Pro version 20.1 and earlier?

Table 2: Intel Quartus Prime Standard Edition Software Fix

Update Details	Impacted Versions	KDB Article (with patches)
Physical synthesis compiler fix (3) <ul style="list-style-type: none">Seed dependent RAM data corruption when compiled in impacted versions of the software.Permanent fix implemented in Intel Quartus Prime Standard edition software ver 20.1 and above.	Ver 18.1 Ver 19.1	Why is there seed dependent RAM data corruption in designs compiled in the Intel® Quartus® Prime Standard edition software version 19.1 and earlier?

Customer Impact of Change and Recommended Action:

Patches for all issues in this customer advisory are available now. Refer to the relevant KDB at the links in table 1 and table 2.

For more information, please contact your local Sales representative, or submit a question or request at the My Intel support page, log-in via:

<https://www.intel.com/content/www/us/en/my-intel/fpga-sign-in.html>

Products Affected / Intel Ordering Codes:

All Intel Arria 10 Devices and Intel Cyclone 10 Devices.

The list of affected part numbers (OPNs) can be downloaded in Excel form:

<https://www.intel.com/content/dam/www/programmable/us/en/pdfs/literature/pcn/adv2103-opn-list.xlsx>

PCN Revision History:

Date of Revision:

March 15, 2021

Revision Number:

00

Reason:

Originally Published PCN



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Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

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