



Product Change Notification

106030 – 04

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Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

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Product Change Notification

Change Notification #: 106030 - 04
Change Title: Intel® Core™ Duo Processors, T2600 (2.16GHz), T2500(2.0 GHz), T2400(1.83GHz), PCN 106030-04, Order Code, C-0 to D-0 Stepping Conversion, Reason for Revision: Adding Additional Product Code/SAP Translation Table Information
Date of Publication: February 22, 2007

Key Characteristics of the Change:

Order Code

Forecasted Key Milestones:

Date of Samples Availability:	Apr 24, 2006
Date Customer Must be Ready to Receive Post-Conversion Material:	Jan 26, 2007
Date of First Availability of Post-Conversion Material:	Jan 26, 2007

The date of "First Availability of Post-Conversion Material" is the projected date that a customer may expect to receive the Post-Conversion Materials. This date is determined by the projected depletion of inventory at the time of the PCN publication. The depletion of inventory may be impacted by fluctuating supply and demand, therefore, although customers should be prepared to receive the Post-Converted Materials on this date, Intel will continue to ship and customers may continue to receive the pre-converted materials until the inventory has been depleted.

Description of Change to the Customer:

Reason for Revision: Adding Product Code Translation Table – Please see Product Code/SAP Translation table below Products Affected / Intel Ordering Codes

In order to provide better product availability, Intel is adjusting the Ready-to-Receive date from June 23, 2006 to January 26, 2007.

In order to incorporate speed enhancements for Yonah Based Processors, Intel is converting the Intel® Core™ Duo Processors T2600, T2500, T2400 from C-0 Stepping to D-0 Stepping.

- A BIOS update is required for D-0 Step Products.
- The D-0 stepping is backwards compatible with the C-0 stepping.
- All C-0 products supported under Intel's Stable Image Platform Program (ISIPP) will continue to be supported on D-0 stepping.

- To improve package quality, D-0 Stepping products use a package with Reverse Geometry Caps. The visible package differences are cosmetic only.

This PCN provides details for products in the Phase 1 Conversion Timeline. Intel plans to conduct conversion of Intel® Core™ Duo Processors, Intel® Core™ Solo Processors and Intel® Celeron® M C-0 processors over three phases. A PCN will be provided for each phase.

Customer Impact of Change and Recommended Action:

- Minimal validation effort for this conversion is expected as there are no logic changes between C-0 Stepping and D-0 Stepping.
- D-0 stepping will require a BIOS Update.
- No platform thermal changes expected.
- Customers should be ready to receive a combination of both D-0 step material and C-0 step material by January 26th, 2007 (See "Date Customer Must be Ready to Receive Post-Conversion Material" above).
- New QDF#, S-Spec, MM numbers will be provided for D-0 Step Qualification Samples and Production units.

Products Affected / Intel Ordering Codes:

Intel® Core™ Duo Processors Tray Products

Product Code	Process or Number	Frequency	Pre-Conversion Production Units		Post-Conversion Production Sample Units		Post-Conversion Production Units	
			S-Spec	MM#	QDF#	MM#	S-Spec	MM#
LF80539GF0482M	T2600	2.16 GHz / 667 MHz	SL8VN	876113	QNNN	879837	SL9JN	882786
LE80539GF0482M	T2600	2.16 GHz / 667 MHz	SL8VS	876117	QPKG	880486	SL9K3	882801
LF80539GF0412M	T2500	2.0 GHz / 667 MHz	SL8VP	876114	QNNX	879843	SL9EH	881984
LE80539GF0412M	T2500	2.0 GHz / 667 MHz	SL8VT	876118	QPKM	880490	SL9K2	882800
LF80539GF0342M	T2400	1.83 GHz / 667 MHz	SL8VQ	876115	QNOH	879866	SL9JM	882785
LE80539GF0342M	T2400	1.83 GHz / 667 MHz	SL8VU	876119	QNSS	879907	SL9JZ	882798

Product Code/SAP Translation Table

Product Name	Product Code on PCN	Product Code on System (SAP)	MM#
Core Duo T2600 (2.16GHz) D-0 stepping (PGA)	LF80539GF0482M SL9JN	LF80539GF0482M X SL9JN	882786
Core Duo T2600 (2.16GHz) D-0 stepping (BGA)	LE80539GF0482M SL9K3	LE80539GF0482M X SL9K3	882801
Core Duo T2500 (2.00GHz) D-0 stepping (PGA)	LF80539GF0412M SL9EH	LF80539GF0412M X SL9EH	881984
Core Duo T2500 (2.00GHz) D-0 stepping (BGA)	LE80539GF0412M SL9K2	LE80539GF0412M X SL9K2	882800
Core Duo T2400 (1.83GHz) D-0 stepping (PGA)	LF80539GF0342M SL9JM	LF80539GF0342M X SL9JM	882785
Core Duo T2400 (1.83GHz) D-0 stepping (BGA)	LE80539GF0342M SL9JZ	LE80539GF0342M X SL9JZ	882798

Boxed Products

Product Code	Process or Number	Frequency	Pre-Conversion Production Units		Post-Conversion Qualification Samples		Post-Conversion Production Units	
			S-Spec	MM#	QDF#	MM#	S-Spec	MM#
BX80539T2600	T2600	2.16 GHz / 667 MHz	SL8VN	878571	QNNN	879837	SL9JN	883284
BX80539T2500	T2500	2.0 GHz / 667 MHz	SL8VP	878609	QNNX	879843	SL9EH	883287
BX80539T2400	T2400	1.83 GHz / 667 MHz	SL8VQ	878611	QNSS	879907	SL9JM	883285

Reference Documents / Attachments:

PCN Revision History:

Date of Revision:	Revision Number:	Reason:
March 31, 2006	00	Originally Published PCN
May 1, 2006	01	Correction to Post-Conversion Product Table
August 23, 2006	02	Adjust the Ready-to-Receive date from June 23, 2006 to January 26, 2007; Product Code Change
January 26, 2007	03	Adding Old Mat ID Translation Table
February 22, 2007	04	Adding Additional Product Code/SAP Translation Table Information