



Product Change Notification

Change Notification #: 116009 - 01
Change Title: Select Intel® Celeron® Processors: J1800, J1900, N2930 & N2807 and Select Intel Atom® Processors: E3845, E3827, E3826, E3825, E3815 & E3805, PCN 116009-01, Product Design, Order Code, Stepping Conversion with new Order Codes, Reason for Revision: Provide Customers document references containing additional information on the D1 Stepping change
Date of Publication: October 09, 2018

Key Characteristics of the Change:

Product Design, Order Code

Forecasted Key Milestones:

Date of Qualification Data Availability:	April 16, 2018
Date Customer Must be Ready to Receive Post-Conversion Material:	September 28, 2018
Date of First Availability of Post-Conversion Material:	March 30, 2018

The date of "First Availability of Post-Conversion Material" is the projected date that a customer may expect to receive the Post-Conversion Materials. This date is determined by the projected depletion of inventory at the time of the PCN publication. The depletion of inventory may be impacted by fluctuating supply and demand, therefore, although customers should be prepared to receive the Post-Converted Materials on this date, Intel will continue to ship and customers may continue to receive the pre-converted materials until the inventory has been depleted.

Description of Change to the Customer:

Reason for Revision: Provide Customers document references containing additional information on the D1 Stepping change

Intel will start transitioning the D-0 stepping of the Intel® Atom® Processor E3800 Series to D-1.
Intel will start transitioning the C-0 stepping of the Intel® Celeron® Processors N2930, N2807, J1800 and J1900 to D-1.

Summary of changes of the D1 stepping:

- 1) The D-1 stepping CPU ID will remain the same as the D-0 stepping.
 - a) The CPUID will report the processor as a D-0 stepping. D-0 and D-1 stepping can be differentiated by reading the Manufacturer ID (PCIE_REG_MANUFACTURER_ID) register field - [B:0, D:31, F:0, Offset:F8h, Bits 23:16]. D-0 stepping processors will have a value of 11h and D-1 stepping processors will have a value of 13h.
- 2) The new D-1 stepping will be physically and electrically drop in compatible, no change form and fit.
- 3) Microcode Compatibility for D1-stepping Intel® Atom™ Processor E3800 Product Family and the Intel® Celeron® Processor N2930, N2807, J1800 and J1900.
 - a) As listed in the Identification Table in the corresponding NDA specification updates (RDC# 539130 and 572021), microcode update (MCU) M0F30679909 (or later) should be used with D1 stepping processors. M013067990x microcode updates, intended for D0 stepping processors only, may work with D1 stepping processors but this combination has not been validated and Intel recommends customers to use M0F30679909 (or later).
- 4) [Please see additional changes made in D-1 stepping listed in the Intel Atom® Processor E3800 Updated documents RDC# 539130 and 572021.](#)

Customer Impact of Change and Recommended Action:

Intel anticipates no impact to customers as a result of the change. The following list provides specific impact to orders:

- 1) New S-Spec and new MM numbers are required for the D1-stepping. See products listed in the "Products Affected/Intel Ordering Codes" table below.
- 2) Customers should be ready to receive the new material listed in the "Products Affected/Intel Ordering Codes" table below by the "Date Customer Must be Ready to Receive Post-Conversion Material" as shown above in the Key Milestones.
- 3) Customer transition to the D1-stepping should be complete by the "Date Customer Must be Ready to Receive Post-Conversion Material" as shown above in the Key Milestones.

The Intel® Celeron® Processors J1800, J1900, N2807 and N2930 are in the process of transitioning support to support to Intel® Embedded Architecture (Refer to PCN116163-00, PCN115146-00, and PCN114864-00). The Intel Atom® Processor E38xx Series is already supported through Intel® Embedded Architecture and the Intel® Embedded Architecture terms, conditions, and pricing are already in effect for all customers. All D1 step will be supported through Intel® Embedded Architecture. Please contact your local Intel® Embedded Architecture Field Sales Representative if you have any further questions.

Products Affected / Intel Ordering Codes:

Marketing Name	Step	Pre Conversion Product Code	Pre Conversion S-Spec	Pre Conversion MM#	Post Conversion Step	Post Conversion Product Code	Post Conversion S-Spec	Post Conversion MM#
Intel Atom® Processor E3805	D0	FH8065301989700	S R20Y	937194	D1	FH8065301989700	S R3V3	962772
Intel Atom® Processor E3815	D0	FH8065301567414	S R1XA	935360	D1	FH8065301567414	S R3V1	962770
Intel Atom® Processor E3825	D0	FH8065301567313	S R1X9	935359	D1	FH8065301567313	S R3UZ	962768

Marketing Name	Step	Pre Conversion Product Code	Pre Conversion S-Spec	Pre Conversion MM#	Post Conversion Step	Post Conversion Product Code	Post Conversion S-Spec	Post Conversion MM#
Intel Atom® Processor E3826	D0	FH8065301542215	S R1X8	935356	D1	FH8065301542215	S R3UX	962766
Intel Atom® Processor E3827	D0	FH8065301487918	S R1X7	935346	D1	FH8065301487918	S R3UV	962764
Intel Atom® Processor E3845	D0	FH8065301487717	S R1X6	935332	D1	FH8065301487717	S R3UT	962762
Intel® Celeron® Processor J1800	C0	FH8065301615104	S R1UU	934012	D1	FH8065301615104	S R3V6	962775
Intel® Celeron® Processor J1900	C0	FH8065301615010	S R1UT	934010	D1	FH8065301615010	S R3V5	962774
Intel® Celeron® Processor N2807	C0	FH8065301730502	S R1W5	934898	D1	FH8065301730502	S R3V8	962777
Intel® Celeron® Processor N2930	C0	FH8065301729501	S R1W3	934896	D1	FH8065301729501	S R3V7	962776

PCN Revision History:

Date of Revision:

March 15, 2018

[October 9, 2018](#)

Revision Number:

00

[01](#)

Reason:

Originally Published PCN

[Provide Customers document references containing additional information on the D1 Stepping change](#)



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The products described in this document may contain design defects or errors known as errata which may cause the product to deviate from published specifications. Current characterized errata are available on request.

Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

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