



# Product Change Notification

## # 102478 - 00

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**Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geography location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.**

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**Change Notification #:** 102478 –00  
**Change Title:** Intel® Xeon™ Processor with 512KB L2 Cache on .13 Micron Process, B-0 Non-Shrink to C-1 Stepping Conversion  
**Date of PCN Publication:** June 28, 2002

**Type of Change Notification:**  
Form-Fit-Function

**Key Characteristics of the Change:**  
Product Design

**Forecasted Key Milestones:**

<b>Date of Samples Availability:</b>	July 15, 2002 – August 2, 2002
<b>Date of Qualification Data Availability:</b>	August 30, 2002
<b>Date Customer Must be Ready to Receive Post-Conversion Material:</b>	September 13, 2002
<b>Date of First Availability of Post-Conversion Material:</b>	September 13, 2002

*The date of "First Availability of Post-Conversion Material" is the projected date that a customer may expect to receive the Post-Conversion Materials. This date is determined by the projected depletion of inventory at the time of the PCN publication. The depletion of inventory may be impacted by fluctuating supply and demand, therefore, although customers should be prepared to receive the Post-Converted Materials on this date, Intel will continue to ship and customers may continue to receive the pre-converted materials until the inventory has been depleted.*

**Description of Change to the Customer:**

Intel® Xeon™ Processor with 512KB L2 Cache on .13 micron process will undergo the following changes for the B-0 Shrink to C-1 core processor stepping change;

- CPUID will change from 0F24h to 0F27h
- Capacitor reduction on package from 15 (B-0 stepping) to 12 caps (C-1 stepping).
- Errata Fixes (Refer to latest revision of the NDA Specification Update for details)
- C-1 step is pin compatible with B-0 step
- C-1 step is expected to have equivalent or better ESD system noise immunity

Please refer to the latest revision of the *Intel® Xeon™ Processor with 512KB L2 Cache on .13 Micron Process Electrical, Mechanical, and Thermal Specifications (EMTS)*, and *Intel® Xeon™ Processor NDA Specification Update* for the latest specification information. *Both will be available on July 2002 from your Intel representative.*

## Customer Impact of Change and Recommended Action:

Although no significant changes are expected in electrical and thermal specifications relative to the B-0 stepping, it is recommended that a full qualification be done with the Intel® Xeon™ processor with 512KB L2 Cache on .13 micron process, with the new C-1 stepping core.

It is recommended that application testing should include verification that major operating systems continue to boot and run confidence tests.

## Products Affected / Intel Ordering Codes:

Intel® Xeon™ Processor with 512KB L2 Cache on .13 Micron Process

Pre-Conversion Product Code		Pre-Conversion Production Units		Qualification Samples		Post-Conversion Production Units	
		B-0 Non-Shrink Stepping				C-1 Stepping	
Product Code	Frequency	S-Spec	MM#	QDF#	MM#	S-Spec	MM#
RN80532KC033512	1.80	SL5Z8	839571	QMM1	847350	SL6EL	847686
RN80532KC041512	2.00	SL5Z9	839575	QMM0	847349	SL6EM	847687
RN80532KC049512	2.20	SL5ZA	839580	QML9	847348	SL6EN	847693
RN80532KC056512	2.40	SL65T	843940	QML8	847347	SL6EP	847695

## Reference Documents / Attachments:

None

## PCN Revision History:

<b>Date of Revision:</b>	<b>Revision Number:</b>	<b>Reason:</b>
	00	Originally Published PCN