



# Product Change Notification

**Change Notification #:** 114067 - 00  
**Change Title:** Intel® Atom™ x7-Z8700, x5-Z8500, x5-Z8300 Processors  
PCN 114067-00  
Product Design, Stepping Conversion  
**Date of Publication:** November 3, 2015

**Key Characteristics of the Change:** Product Design

## Forecasted Key Milestones:

Milestone	Date
Date of Samples Availability:	11/9/15
Date of Qualification Data Availability:	12/3/15
Date of First Availability of Post-Conversion Material:	2/8/16
Date Customer Must be Ready to Receive Post-Conversion Material:	9/30/16

*The date of "First Availability of Post-Conversion Material" is the projected date that a customer may expect to receive the Post-Conversion Materials. This date is determined by the projected depletion of inventory at the time of the PCN publication. The depletion of inventory may be impacted by fluctuating supply and demand, therefore, although customers should be prepared to receive the Post-Converted Materials on this date, Intel will continue to ship and customers may continue to receive the pre-converted materials until the inventory has been depleted.*

## Description of Change to the Customer:

Intel will start transitioning from C-stepping to D-stepping for Intel® Atom™ x7-Z8700, Intel® Atom™ x5-Z8500, and Intel® Atom™ x5-Z8300 processors.

Summary of hardware & software changes from Intel® Atom™ x5/x7 processor C-stepping to D-stepping:

1. Increase max. CPU burst frequency
2. Fix 2 previously disclosed Alpha Blending and ASTC graphics errata
3. Implement new processor numbers with "50" added to processor number to distinguish the new D-stepping parts (ex. C-stepping: Intel® Atom™ x7-Z8700 -> D-stepping: Intel® Atom™ x7-Z8750)
4. Introduce new graphics branding with Intel® HD Graphics 400 for 12EU SKUs (Intel® Atom™ x5-Z8550 and Intel® Atom™ x5-Z8350), and Intel® HD Graphics 405 for 16EU SKU (Intel® Atom™ x7-Z8750). The new Intel® HD Graphics 400/405 branding is only exposed via the Intel® HD Graphics Control Panel under Windows OS.

## Customer Impact of Change and Recommended Action:

Qualification samples will be provided to enable requalification and/or validation of the redefined part(s).

1. Intel anticipates minimal validation.
2. New S-Spec and new MM numbers are required for the D-stepping. See products listed in the "Products Affected/Intel Ordering Codes" table below.

Customers should be ready to receive the new material listed in the "Products Affected/Intel Ordering Codes" table below by the "Date Customer Must be Ready to Receive Post-Conversion Material" as shown above in the Key Milestones.

Please contact your Intel Field Sales Representative with any questions, requests or concerns.

**Affected / Intel Ordering Codes:**

Pre Processor #	Pre Max. CPU Burst Frequency #	Pre Conversion Product Code	Pre Conversion S-Spec	Pre Conversion MM#	Post Conversion Qualification Samples QDF#	Post Conversion Qualification Samples MM#	Post Processor #	Post Max. CPU Burst Frequency #	Post Conversion Product Code	Post Conversion S-Spec	Post Conversion MM#
x7-Z8700	2.4 GHZ	FJ8066401715809	S R27M	940953	QK1E	945048	x7-Z8750	2.56 GHZ	FJ8066401715827	S R2KG	947016
x7-Z8700	2.4 GHZ	FJ8066401715825	S R29W	943102	QK1E	945048	x7-Z8750	2.56 GHZ	FJ8066401715827	S R2KG	947016
x5-Z8500	2.24 GHZ	FJ8066401715814	S R27N	940954	QK1F	945049	x5-Z8550	2.4 GHz	FJ8066401715843	S R2KH	947017
x5-Z8500	2.24 GHZ	FJ8066401715842	S R2GN	944708	QK1G	945050	x5-Z8550	2.4 GHz	FJ8066401715844	S R2KJ	947018
x5-Z8300	1.84 GHZ	FJ8066401836609	S R29Z	943165	QK1H	945051	x5-Z8350	1.92 GHz	FJ8066401836620	S R3KT	947027

**PCN Revision History:**

**Date of Revision:**  
November 3, 2015

**Revision Number:**  
00

**Reason:**  
Originally Published PCN



# Product Change Notification

## #114067- 00

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**Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.**

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