



# Product Change Notification

**Change Notification #:** 118929-00  
**Change Title:** All SoC FPGA devices,  
PCN 118929-00, Documentation,  
Pin Connection Guidelines (PCG) Updates  
**Date of Publication:** February 28, 2022

## Key Characteristics of the Change:

Documentation

## Forecasted Key Milestones:

Availability of Intel® SoC FPGA PCG Document Updates	Now
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## Description of Change to the Customer:

Intel is notifying customers of important updates to the Pin Connection Guidelines (PCG) of all Intel® SoC FPGA devices. Please note that there is no change to the design of the devices.

Below are the most recent updates to the Intel PCG document.

Product Family	Pin Connection Guidelines (PCG) Updates
Intel® Stratix®10	<p>Updated the pin description and connection guidelines of the NAND_RB pin (Refer to Table 27 of page 60-61).</p> <p>Link to PCG:  <a href="https://www.intel.com/content/www/us/en/docs/programmable/683028/current/device-family-pin-connection-guidelines.html">https://www.intel.com/content/www/us/en/docs/programmable/683028/current/device-family-pin-connection-guidelines.html</a></p>
Intel® Agilex®	<p>Updated the pin description and connection guidelines of the NAND_RB pin (Refer to Table 27 of page 49-50).</p> <p>Link to PCG <a href="https://www.intel.com/content/www/us/en/docs/programmable/683112/current/device-family-pin-connection-guidelines.html">https://www.intel.com/content/www/us/en/docs/programmable/683112/current/device-family-pin-connection-guidelines.html</a></p>
Intel® Arria®10	<p>Updated the NAND_RB connection guidelines of the HPS_DEDICATED_12 pin (Refer to Table 14 of page 36).</p> <p>Updated the NAND_RB connection guidelines of the HPS_Shared_Q2_2 pin in (Table 15 of page 46).</p> <p>Updated the NAND_RB connection guidelines of the HPS_Shared_Q4_2 pin in (Table 15 of page 56).</p> <p>Link to PCG  <a href="https://www.intel.com/content/www/us/en/docs/programmable/683814/current/intel-arria-10-gx-gt-and-sx-device-family.html">https://www.intel.com/content/www/us/en/docs/programmable/683814/current/intel-arria-10-gx-gt-and-sx-device-family.html</a></p>
Intel® Cyclone® V	<p>Updated the connection guidelines of the NAND_RB pin (Refer to page 32).</p> <p>Link to PCG: <a href="https://www.intel.com/content/www/us/en/products/details/fpga/cyclone/v/docs.html?s=Newest&amp;q=654351">https://www.intel.com/content/www/us/en/products/details/fpga/cyclone/v/docs.html?s=Newest&amp;q=654351</a></p>
Intel® Arria® V	<p>Updated the connection guidelines of the NAND_RB pin (Refer to page 32)</p> <p>Link to PCG:  <a href="https://www.intel.com/content/www/us/en/products/details/fpga/arria/v/docs.html?s=Newest&amp;q=653758">https://www.intel.com/content/www/us/en/products/details/fpga/arria/v/docs.html?s=Newest&amp;q=653758</a></p>

## **Customer Impact of Change and Recommended Action:**

Customers are requested to review the change and determine the impact on their designs. Refer to the revision history of the PCG documents for complete list and history of updates.

For question or support, please contact your local Sales representative, or submit a question or request at the My Intel support page, log-in via:

<https://www.intel.com/content/www/us/en/my-intel/fpga-sign-in.html>

## **Products Affected/Intel Ordering Codes:**

The list of affected part numbers (OPNs) can be downloaded in Excel form:

<https://www.intel.com/content/dam/www/programmable/us/en/pdfs/literature/pcn/adv2204-opn-list.xlsx>

## **PCN Revision History:**

<b>Date of Revision:</b>	<b>Revision Number:</b>	<b>Reason:</b>
February 28, 2022	00	Originally Published PCN



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**Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.**

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