



Product Change Notification

103126 – 00

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Product Change Notification

Change Notification #: 103126 - 00
Change Title: Desktop Intel® Celeron® Processor with 128KB L2 Cache on .13 Micron Process, PCN 103126-00, FFF, C-1 to D-1 Stepping Conversion
Date of Publication: March 27, 2003

Type of Change Notification:
FFF - (Form-Fit-Function)

Key Characteristics of the Change:
Product Design

Forecasted Key Milestones:

Date of Samples Availability:	Apr 21, 2003
Date of Qualification Data Availability:	Jun 06, 2003
Date Customer Must be Ready to Receive Post-Conversion Material:	Jun 27, 2003
Date of First Availability of Post-Conversion Material:	Jun 27, 2003

The date of "First Availability of Post-Conversion Material" is the projected date that a customer may expect to receive the Post-Conversion Materials. This date is determined by the projected depletion of inventory at the time of the PCN publication. The depletion of inventory may be impacted by fluctuating supply and demand, therefore, although customers should be prepared to receive the Post-Converted Materials on this date, Intel will continue to ship and customers may continue to receive the pre-converted materials until the inventory has been depleted.

Description of Change to the Customer:

Intel® Celeron® Processor on .13 Micron Process in the 478-pin package will undergo the following changes:

- C-1 to D-1 core processor stepping change
- New s-specs for affected product line items
- CPUID will change from 0F27h for C-1 step to 0F29h for D-1 step
- Electrical, mechanical, and thermal specification qualification required. Specifications are within the designated FMB guidelines.
- Capacitor count/type will change from twelve 2 uF idc805 caps to sixteen 1 uF 603 (2 terminal) caps.
- The D-1 stepping is also Multiple VID (1.475V, 1.500V, and 1.525V) and will have one new s-spec and material master number per frequency.
- Impedance pin functionality added (pin AE26). No impact to existing platforms.

Please refer to the future revision of the *Intel® Celeron® Processor on .13 Micron Process in the 478-Pin Package Electrical, Mechanical, and Thermal Specifications (EMTS)*, and *Intel® Celeron® Processor in the 478-Pin Package NDA Specification Update* for the latest specification information. Both will be available from your Intel representative in mid-April 2003.

Customer Impact of Change and Recommended Action:

It is recommended that application testing be completed on the new stepping. Testing should include verification that major operating systems continue to boot and run confidence tests.

Samples will be available on one frequency-limited* fuse configuration: the 2.40 GHz. To validate lower frequencies, customers are requested to down-clock samples to lower frequencies facilitating validation of systems at the lower frequencies.

* Frequency-limited means that samples will operate up to, but not above, their specified frequency. Therefore, higher frequency processors may be operated at lower frequencies for the validation of lower specified frequency processors.

Products Affected / Intel Ordering Codes:

Desktop Intel® Celeron® Processor with 128KB L2 Cache on .13 Micron Process

Product Code	Frequency	Pre-Conversion Production Units		Post-Conversion Qualification Samples		Post-Conversion Production Units	
		S-Spec	MM #	QDF #	MM #	S-Spec	MM #
RK80532RC041128	2 GHz	S L6RV	851346	N/A	N/A	S L6VR	852181
RK80532RC045128	2.10 GHz	S L6RS	851411	N/A	N/A	S L6VS	852929
RK80532RC049128	2.20 GHz	S L6RW	851345	N/A	N/A	S L6VT	852182
RK80532RC052128	2.30 GHz	S L6XJ	853091	N/A	N/A	S L6WC	852928
RK80532RC056128	2.40 GHz	S L6V2	852525	QVW5	853845	S L6VU	852236

Reference Documents / Attachments:

Document:

Location #:

PCN Revision History:

Date of Revision:

March 27, 2003

Revision Number:

00

Reason:

Originally Published PCN