



Product Change Notification

106896 - 01

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Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

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Product Change Notification

Change Notification #: 106896 - 01
Change Title: Intel® Core™2 Duo mobile processors T5500 & T5600, PCN 106896-01, Product Design, B-2 to L-2 stepping Conversion, Reason for Revision: Correction to Post Conversion S-Specs.
Date of Publication: November 08, 2006

Key Characteristics of the Change:

Product Design

Forecasted Key Milestones:

Date of Samples Availability:	Nov 17, 2006
Date of Qualification Data Availability:	Jan 19, 2007
Date Customer Must be Ready to Receive Post-Conversion Material:	Feb 02, 2007
Date of First Availability of Post-Conversion Material:	Jan 05, 2007

The date of "First Availability of Post-Conversion Material" is the projected date that a customer may expect to receive the Post-Conversion Materials. This date is determined by the projected depletion of inventory at the time of the PCN publication. The depletion of inventory may be impacted by fluctuating supply and demand, therefore, although customers should be prepared to receive the Post-Converted Materials on this date, Intel will continue to ship and customers may continue to receive the pre-converted materials until the inventory has been depleted.

Description of Change to the Customer:

Reason for Revision: Correction to Post Conversion S-Specs.

Intel® Core™2 Duo mobile processors T5600 and T5500 will undergo the following changes for the B-2 Stepping to L-2 Stepping core processor stepping change:

CPUID will change. Merom L-2 Stepping will require a Micro-Code Update.

Die size optimized for manufacturability; No platform thermal changes expected to platforms designed to Intel guidelines.

Minor visible difference between the B-2 and L-2 packages

Merom L-2 Stepping is pin compatible with Merom B-2 Stepping

New QDF, S-Spec, & MM numbers will be provided for Merom L-2 Step Qualification Samples & Production units

Please refer to the Electrical, Mechanical, and Thermal Specifications 2.2 - NDA (EMTS), for the latest specification information.

Customer Impact of Change and Recommended Action:

Intel anticipates no changes to customer platforms designed to Intel guidelines.

Refer to Intel® Core™2 Duo mobile Processors T5500 and T5600 Electrical, Mechanical, and Thermal Specifications.

As there are no feature changes between Merom L-2 Stepping and Merom B-2 Stepping, minimal validation effort for this conversion is expected. Intel® Core™2 Duo mobile processor T5600 & T5500 Merom L-2 stepping will require a Micro-Code Update. Customers should be ready to receive Merom L-2 stepping material by Feb 02, 2007.

Products Affected / Intel Ordering Codes:

PGA

Processor#	Frequency	Pre Conversion Product Code	Pre Conversion S-Spec	Pre Conversion MM#	Post Conversion Qualification Samples QDF#	Post Conversion Qualification Samples MM#	Post Conversion Product Code	Post Conversion S-Spec	Post Conversion MM#
T5500	1.66GHz	LF80537GF0282M	S L9SH	884325	QUGC	885400	LF80537GF0282M	S L9U4	885188
T5600	1.83GHz	LF80537GF0342M	S L9SG	884326	QUFU	885385	LF80537GF0342M	S L9U3	885187

BGA

Processor#	Frequency	Pre Conversion Product Code	Pre Conversion S-Spec	Pre Conversion MM#	Post Conversion Qualification Samples QDF#	Post Conversion Qualification Samples MM#	Post Conversion Product Code	Post Conversion S-Spec	Post Conversion MM#
T5600	1.83GHz	LE80537GF0342M	S L9SP	884340	QUGK	885418	LE80537GF0342M	S L9U7	885242
T5500	1.66GHz	LE80537GF0282M	S L9SQ	884341	QUGO	885892	LE80537GF0282M	S L9U8	885241

Reference Documents / Attachments:

Document:

Location #:

PCN Revision History:

Date of Revision:

Revision Number:

Reason:

October 27, 2006

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Originally Published PCN

November 8, 2006

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Correction to Post Conversion S-Specs.