



# Product Change Notification

## 109288 - 00

Information in this document is provided in connection with Intel products. No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document. Except as provided in Intel's Terms and Conditions of Sale for such products, Intel assumes no liability whatsoever, and Intel disclaims any express or implied warranty, relating to sale and/or use of Intel products including liability or warranties relating to fitness for a particular purpose, merchantability, or infringement of any patent, copyright or other intellectual property right. Intel products are not intended for use in medical, life saving, or life sustaining applications. Intel may make changes to specifications and product descriptions at any time, without notice.

Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

**Americas Contact:** [asmo.pcn@intel.com](mailto:asmo.pcn@intel.com)

**Asia Pacific Contact:** [apacgccb@intel.com](mailto:apacgccb@intel.com)

**Europe Email:** [eccb@intel.com](mailto:eccb@intel.com)

**Japan Email:** [jccb.ijkk@intel.com](mailto:jccb.ijkk@intel.com)

Copyright © Intel Corporation 2009. Other names and brands may be claimed as the property of others.

Celeron, Centrino, Intel, the Intel logo, Intel Core, Intel NetBurst, Intel NetMerge, Intel NetStructure, Intel SingleDriver, Intel SpeedStep, Intel StrataFlash, Intel Viiv, Intel XScale, Itanium, MMX, Paragon, PDCharm, Pentium, and Xeon are trademarks or registered trademarks of Intel Corporation or its subsidiaries in the United States and other countries.

Learn how to use Intel Trade Marks and Brands correctly at <http://www.intel.com/intel/legal/tmusage2.htm>.



# Product Change Notification

**Change Notification #:** 109288 - 00  
**Change Title:** DG35EC, PCN 109288-00, Product Design, Component Change  
**Date of Publication:** March 18, 2009

**Key Characteristics of the Change:**  
Product Design

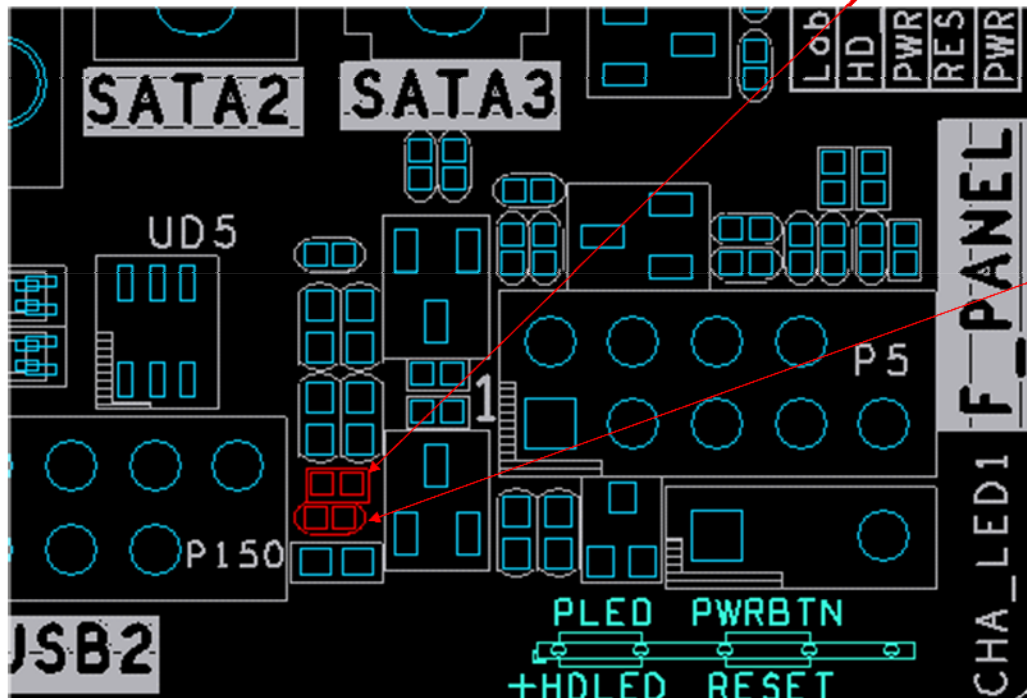
## Forecasted Key Milestones:

<b>Date Customer Must be Ready to Receive Post-Conversion Material:</b>	Apr 01, 2009
---	--------------

## Description of Change to the Customer:

The 0.1uF capacitor at location SC68 has been changed from +/- 20% to +/-10% to enhance the board design and Real-time clock margins.

SC68, change from  
0.1uF +/-20% to +/-10%



SR16, no  
change

### **Customer Impact of Change and Recommended Action:**

This change has been thoroughly evaluated to ensure that there is no quality, reliability or functional implications to our customers. Intel is not recommending additional qualification of these changes on platforms received from Intel.

### **Products Affected / Intel Ordering Codes:**

<b>Pre Change Product Code</b>	<b>Pre Change MM#</b>	<b>Pre Change AA</b>	<b>Post Change AA</b>
BOXDG35EC 896207	896207	E29266-208	E29266-209
BLKDG35EC 896208	896208	E29266-208	E29266-209
LADG35EC 897336	897336	E37012-208	E37012-209
BOXDG35ECC 898194	898194	E39017-208	E39017-209
BLKDG35ECC 898195	898195	E39017-208	E39017-209

### **Reference Documents / Attachments:**

**Document:**

**Location #:**

### **PCN Revision History:**

**Date of Revision:**

March 18, 2009

**Revision Number:**

00

**Reason:**

Originally Published PCN