



Product Change Notification

110761 - 00

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Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

Americas Contact: asmo.pcn@intel.com

Asia Pacific Contact: apacgccb@intel.com

Europe Email: eccb@intel.com

Japan Email: jccb.ijkk@intel.com

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Product Change Notification

Change Notification #: 110761 - 00
Change Title: Intel® Atom™ Processors E645C, E645CT, E665C, E665CT, PCN 110761-00, Product Design, B0 to B1
Date of Publication: Sept 1, 2011

Key Characteristics of the Change:

Product Design

Forecasted Key Milestones:

Date of Qualification Data Availability:	Nov 07, 2011
Date of First Availability of Post-Conversion Material:	Oct 31, 2011
Date Customer Must be Ready to Receive Post-Conversion Material:	Jan 30, 2012

The date of "First Availability of Post-Conversion Material" is the projected date that a customer may expect to receive the Post-Conversion Materials. This date is determined by the projected depletion of inventory at the time of the PCN publication. The depletion of inventory may be impacted by fluctuating supply and demand, therefore, although customers should be prepared to receive the Post-Converted Materials on this date, Intel will continue to ship and customers may continue to receive the pre-converted materials until the inventory has been depleted.

A customer may request an extension to receive pre-conversion material after the Ready-To-Receive (RTR) date contained in this Product Change Notification. Provided Intel can support the volume and timing of the extension request, all confirmed extension support must be booked by the customer within 7 days of receiving confirmation or the volume will be made available to support other requests. All requests are supported on a first come, first serve basis.

Unless otherwise agreed to in writing by Intel, all material booked on orders corresponding to the approved extension requests beyond RTR date are Non Cancellable, Non returnable (NCNR). Please contact your local Intel representative for product availability.

Description of Change to the Customer:

The Intel® Atom™ Processor E6x5C series will undergo the following changes for the B-0 to B-1 processor stepping changes:

- B-1 is pin compatible with B-0
- Intel anticipates no changes to customer platforms designed to Intel guidelines.
- Customer BIOS change will be required to incorporate the latest Chipset Microcode Update (CMC) to support B-1 stepping.
- New Product order number (Intel MM#) and S-spec marking - See Pre and Post conversion information in the Products Affected / Intel Ordering Codes section below.

Customer Impact of Change and Recommended Action:

The changes to the silicon die consist of metal layer routing changes only. The Intel® Atom™ Processor E6x5C B-1 stepping will require a BIOS update to incorporate the latest Chipset Microcode Update (CMC).

For the fix to errata BI43, customers would have implemented a BIOS change to disable the VRM and a circuit change for the VccRTC pin in order to use B-0. With the fix implemented in B-1, Customers could, but are not required, to remove the errata documented circuit change. If a customer chooses to remove the circuit then a BIOS change will be required to enable the VRM.

Products Affected / Intel Ordering Codes:

Intel® Atom™ Processors								
Pre Conversion					Post Conversion			
Processor#	Product Code	S-Spec	MM#	Stepping	Product Code	S-Spec	MM#	Stepping
E665CT	CY80632007224AA	S LH9H	908901	B0	CY80632007224AA	S LJBW	916385	B1
E645CT	CY80632007221AA	S LH9J	908902	B0	CY80632007221AA	S LJBX	916387	B1
E6665C	CY80632007224AB	S LH9X	909452	B0	CY80632007224AB	S LJBZ	916391	B1
E645C	CY80632007221AB	S LH9Y	909455	B0	CY80632007221AB	S LJB2	916393	B1

PCN Revision History:

Date of Revision:
Sept 1, 2011

Revision Number:
00

Reason:
Originally Published PCN