



Product Change Notification

108701 - 00

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Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

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Product Change Notification

Change Notification #: 108701 - 00
Change Title: Boxed Quad-Core Intel® Xeon® processor (Harpertown), PCN 108701-00, Product Design, C-0 to E-0 Stepping Conversion for Boxed product
Date of Publication: August 1, 2008

Key Characteristics of the Change:

Product Design

Forecasted Key Milestones:

Date Customer Must be Ready to Receive Post-Conversion Material:	Sep 05, 2008
Date of First Availability of Post-Conversion Material:	Aug 29, 2008

The date of "First Availability of Post-Conversion Material" is the projected date that a customer may expect to receive the Post-Conversion Materials. This date is determined by the projected depletion of inventory at the time of the PCN publication. The depletion of inventory may be impacted by fluctuating supply and demand, therefore, although customers should be prepared to receive the Post-Converted Materials on this date, Intel will continue to ship and customers may continue to receive the pre-converted materials until the inventory has been depleted.

Description of Change to the Customer:

Boxed Quad-Core Intel® Xeon® processor (Harpertown):

- New SSPEC and MM numbers for the converting products
- CPUID will change to: 0x1067A, IDCODE will change to: 0xA005b013
- Package change to Halide free package
- C-0 package is pin compatible with E-0 package
- Minor feature set changes with E0 stepping - added ACNT2 support and XSAVE/XRSTOR instructions support
- There are no changes to Electrical, Mechanical and Thermal processor Specifications
- Intel anticipates no changes to customer platforms designed to Intel guidelines

Customer Impact of Change and Recommended Action:

There are minor feature set changes between the C-0 and E-0 steppings.

The Boxed Quad-Core Intel® Xeon® processor (Harpertown) stepping will require motherboard manufacturers to update their BIOS to support the new E-0 processor stepping. Once customers implement the BIOS update, they will be able to accept both stepping materials. Customers should be ready to receive a combination of both stepping materials by the "Date Customer Must be Ready to Receive Post-Conversion Material" above. For customers implementing their own BIOS, it is necessary to

incorporate the latest BIOS to support the new steppings. Please contact your local Intel representative for further technical questions.

Products Affected / Intel Ordering Codes:

Processor #	Pre-Conversion Product Code	Pre - Conversion MM#	Post - Conversion Product Code	Post-Conversion MM#
X5460	BX80574X5460A SLANP	894817	BX80574X5460A SLBBA	898970
X5460	BX80574X5460P SLANP	894818	BX80574X5460P SLBBA	898971
X5450	BX80574X5450A SLASB	894819	BX80574X5450A SLBBE	898972
X5450	BX80574X5450P SLASB	894820	BX80574X5450P SLBBE	898973
E5450	BX80574E5450A SLANQ	894821	BX80574E5450A SLBBM	898974
E5450	BX80574E5450P SLANQ	894822	BX80574E5450P SLBBM	898976
E5440	BX80574E5440A SLANS	894823	BX80574E5440A SLBBJ	898977
E5440	BX80574E5440P SLANS	894824	BX80574E5440P SLBBJ	898975
E5430	BX80574E5430A SLANU	894825	BX80574E5430A SLBBK	898978
E5430	BX80574E5430P SLANU	894826	BX80574E5430P SLBBK	898979
E5420	BX80574E5420A SLANV	894828	BX80574E5420A SLBBL	898982
E5420	BX80574E5420P SLANV	894827	BX80574E5420P SLBBL	898983
E5410	BX80574E5410A SLANW	894829	BX80574E5410A SLBBC	898986
E5410	BX80574E5410P SLANW	894830	BX80574E5410P SLBBC	898985
E5405	BX80574E5405A SLAP2	894832	BX80574E5405A SLBBP	898987
E5405	BX80574E5405P SLAP2	894831	BX80574E5405P SLBBP	898984
L5420	BX80574L5420A SLARP	894833	BX80574L5420A SLBBR	898988
L5420	BX80574L5420P SLARP	894834	BX80574L5420P SLBBR	898990
L5410	BX80574L5410A SLAP4	894836	BX80574L5410A SLBBS	898991
L5410	BX80574L5410P SLAP4	894835	BX80574L5410P SLBBS	898989

Reference Documents / Attachments:

Document:

Location #:

PCN Revision History:

Date of Revision:

Revision Number:

Reason:

August 1, 2008

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Originally Published PCN