



Product Change Notification

109269 - 00

Information in this document is provided in connection with Intel products. No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document. Except as provided in Intel's Terms and Conditions of Sale for such products, Intel assumes no liability whatsoever, and Intel disclaims any express or implied warranty, relating to sale and/or use of Intel products including liability or warranties relating to fitness for a particular purpose, merchantability, or infringement of any patent, copyright or other intellectual property right. Intel products are not intended for use in medical, life saving, or life sustaining applications. Intel may make changes to specifications and product descriptions at any time, without notice.

Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

Americas Contact: asmo.pcn@intel.com

Asia Pacific Contact: apacgccb@intel.com

Europe Email: eccb@intel.com

Japan Email: jccb.ijkk@intel.com

Copyright © Intel Corporation 2008. Other names and brands may be claimed as the property of others.

Celeron, Centrino, Intel, the Intel logo, Intel Core, Intel NetBurst, Intel NetMerge, Intel NetStructure, Intel SingleDriver, Intel SpeedStep, Intel StrataFlash, Intel Viiv, Intel XScale, Itanium, MMX, Paragon, PDCharm, Pentium, and Xeon are trademarks or registered trademarks of Intel Corporation or its subsidiaries in the United States and other countries.

Learn how to use Intel Trade Marks and Brands correctly at <http://www.intel.com/intel/legal/tmusage2.htm>.



Product Change Notification

Change Notification #: 109269 - 00

Change Title: Boxed Intel® Pentium® Processor E5200, PCN 109269-00, Product Design, M-0 to R-0 Stepping Conversion

Date of Publication: March 13, 2009

Key Characteristics of the Change:

Product Design

Forecasted Key Milestones:

Date Customer Must be Ready to Receive Post-Conversion Material:	April 13, 2009
Date of First Availability of Post-Conversion Material:	April 13, 2009

The date of "First Availability of Post-Conversion Material" is the projected date that a customer may expect to receive the Post-Conversion Materials. This date is determined by the projected depletion of inventory at the time of the PCN publication. The depletion of inventory may be impacted by fluctuating supply and demand, therefore, although customers should be prepared to receive the Post-Converted Materials on this date, Intel will continue to ship and customers may continue to receive the pre-converted materials until the inventory has been depleted.

Description of Change to the Customer:

Intel is initiating an M-0 to R-0 conversion for Intel® Pentium® processor E5200 dual-core where they will undergo the following changes:

- New S-spec and MM numbers for the converting products
- CPUID will change from 10677 to 1067A
- Power State Indicator (PSI) support with Intel 4 series chipsets
- New instructions added – XSAVE/XRSTOR
- Package change to halide free package
- R-0 package is pin compatible with M-0 package

Customer Impact of Change and Recommended Action:

Intel anticipates no change to customer platforms designed to Intel guidelines. For details of the new features and power states, please refer to Intel® Pentium® dual-core Processor E5000 Series Electrical, Mechanical and Thermal Specifications (EMTS)

Minimal re-qualification and/or validation is expected for features already supported on M-0 stepping. These processors will require a BIOS change, because of the new CPUID. Once customers implement the BIOS update, they will be able to accept both M-0 and R-0 stepping material. Customers should be ready to receive a combination of both M-0 stepping material and R-0 stepping material by the “Date Customer Must be Ready to Receive Post-Conversion Material” above. For customers implementing their own BIOS, it is necessary to incorporate the latest Microcode Update (MCU) to support the R-0 stepping. Please contact your local Intel representative for further technical questions.

Products Affected / Intel Ordering Codes:

Boxed Intel® Pentium dual-core E5200 Processor									
Processor #	GHz	Pre Conversion				Post Conversion			
		Product Code	S-Spec	MM#	Stepping	Product Code	S-Spec	MM#	Stepping
E5200	2.50 Ghz	BX80571E5200	SLAY7	899647	M-0	BX80571E5200	SLB9T	902389	R-0
E5200	2.50 Ghz	BXC80571E5200	SLAY7	899650	M-0	BXC80571E5200	SLB9T	902390	R-0

Reference Documents / Attachments:

Document:

Location #:

PCN Revision History:

Date of Revision:

March 13, 2009

Revision Number:

00

Reason:

Originally Published PCN