



Product Change Notification

Change Notification #: 119228-00

Change Title: Select Intel® Arria® 10 & Intel® Cyclone® 10 Devices,
PCN 119228-00, Documentation,
Device Updates

Date of Publication: August 24, 2022

Key Characteristics of the Change:

Documentation

Forecasted Key Milestones:

Date Customer Must be Ready to Receive Post-Conversion Material	Available Now
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Description of Change to the Customer:

Intel® is notifying customers of important updates to Intel® Arria® 10 and Intel® Cyclone®10 Devices, as communicated previously via ADV2224 on August 16, 2022.

Refer to Table 1 for the list of major updates and further details.

Table 1

#	Link to Updated Documents	Update Details
1)	Intel® Arria® 10 SX Device Errata and Design Recommendations. Refer to Page 20.	Added a new erratum: GPIO Output may not meet the On-Chip Series Termination (Rs OCT) without Calibration Resistance Tolerance Specification or Current Strength Expectation.
2)	Intel® Arria® 10 GX/GT Device Errata and Design Recommendations Refer to Page 10.	
3)	Intel® Cyclone® 10 GX Device Errata and Design Guidelines. Refer to Page 7.	

Customer Impact of Change and Recommended Action:

Customers are requested to review the changes and determine the impact on their designs. Refer to the revision history of the updated documents for a complete list and history of updates.

For questions or support, please contact your local Sales representative, or submit a question or request at the My Intel support page, log-in via:

<https://www.intel.com/content/www/us/en/my-intel/fpga-sign-in.html>

Products Affected/Intel Ordering Codes:

Intel Arria 10 SX, GX, GT and Cyclone 10 GX Devices.

The list of affected part numbers (OPNs) can be downloaded in Excel form:

<https://cdrdv2.intel.com/v1/dl/getContent/740055>

PCN Revision History:

Date of Revision:

August 24, 2022

Revision Number:

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Reason:

Originally Published PCN



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No response from customers will be deemed an acceptance of the change, and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below.

Americas Contact: asm0.pcn@intel.com

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