



Product Change Notification

109227 - 00

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Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

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Product Change Notification

Change Notification #: 109227 - 00

Change Title: Tray Intel® Pentium dual-core E5200 Processor,
PCN 109227-00, Product Design, M-0 to R-0
Stepping Conversion

Date of Publication: February 25, 2009

Key Characteristics of the Change:

Product Design

Forecasted Key Milestones:

Date of Samples Availability:	February 25 – March 11, 2009**
Date of Qualification Data Availability:	February 25, 2009
Date Customer Must be Ready to Receive Post-Conversion Material:	May 27, 2009
Date of First Availability of Post-Conversion Material:	April 13, 2009

The date of "First Availability of Post-Conversion Material" is the projected date that a customer may expect to receive the Post-Conversion Materials. This date is determined by the projected depletion of inventory at the time of the PCN publication. The depletion of inventory may be impacted by fluctuating supply and demand, therefore, although customers should be prepared to receive the Post-Converted Materials on this date, Intel will continue to ship and customers may continue to receive the pre-converted materials until the inventory has been depleted.

**Note: customers received E5000 R step 2.7GHz samples that can be down clocked to validate E5200 R step 2.5GHz (see: DCL <http://www.intel.com/cd/edesign/library/asmoma/eng/402240.htm> from September 12, 2008).

Description of Change to the Customer:

Intel is initiating an M-0 to R-0 conversion for Intel® Pentium dual-core E5200

Processor where they will undergo the following changes:

New S-spec and MM numbers for the converting products

CPUID will change from 10677 to 1067A

Power State Indicator (PSI) support with Intel 4 series chipsets

New instructions added – XSAVE/XRSTOR

Package change to halide free package

R-0 package is pin compatible with M-0 package

Customer Impact of Change and Recommended Action:

Intel anticipates no change to customer platforms designed to Intel guidelines. For details of the new features and power states, please refer to Intel® Pentium® dual-core Processor E5000 Series Electrical, Mechanical and Thermal Specifications (EMTS)

Minimal re-qualification and/or validation is expected for features already supported on M-0 stepping. These processors will require a BIOS change, because of the new CPUID. Once customers implement the BIOS update, they will be able to accept both M-0 and R-0 stepping material. Customers should be ready to receive a combination of both M-0 stepping material and R-0 stepping material by the “Date Customer Must be Ready to Receive Post-Conversion Material” above. For customers implementing their own BIOS, it is necessary to incorporate the latest Microcode Update (MCU) to support the R-0 stepping. Please contact your local Intel representative for further technical questions.

Products Affected / Intel Ordering Codes:

Intel® Pentium dual-core E5200 Processor

Processor #	GHz	Pre Conversion				Qualification Samples		Post Conversion			
		Product Code	S-Spec	MM#	Stepping	QDF#	MM#	Product Code	S-Spec	MM#	Stepping
E5200	2.50 Ghz	EU80571PG0602M	SLAY7	896940	M-0	QJLU**	899106	AT80571PG0602M	SLB9T	898338	R-0

**Customers received E5000 series R step 2.7GHz samples (see: DCL at <http://www.intel.com/cd/edesign/library/asmo-na/eng/402240.htm>). The samples are frequency limited but unlocked and can be used for E5200 R-0 stepping validation. Intel recommends that customers down clock these samples to validate E5200 R-0 step 2.5GHz.

Reference Documents / Attachments:

Document:

Location #:

PCN Revision History:

Date of Revision:

February 25, 2009

Revision Number:

00

Reason:

Originally Published PCN