



# Product Change Notification

## 106585 - 00

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Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

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# Product Change Notification

**Change Notification #:** 106585 - 00  
**Change Title:** Dual Core Intel® Itanium® 2 Processor 9000 series, PCN 106585-00, Product Design, C1 to C2 Stepping Change  
**Date of Publication:** July 30, 2006

## Key Characteristics of the Change:

Product Design

## Forecasted Key Milestones:

<b>Date Customer Must be Ready to Receive Post-Conversion Material:</b>	Aug 30, 2006
<b>Date of First Availability of Post-Conversion Material:</b>	Aug 04, 2006

*The date of "First Availability of Post-Conversion Material" is the projected date that a customer may expect to receive the Post-Conversion Materials. This date is determined by the projected depletion of inventory at the time of the PCN publication. The depletion of inventory may be impacted by fluctuating supply and demand, therefore, although customers should be prepared to receive the Post-Converted Materials on this date, Intel will continue to ship and customers may continue to receive the pre-converted materials until the inventory has been depleted.*

## Description of Change to the Customer:

Dual Core Intel® Itanium® 2 Processor 9000 series will undergo the following changes for the C-1 to the C-2 core processor stepping change:

- CPUID will change from 0x2005 to 0x2007
- C-1 is pin compatible with C-2
- New S-Specs for converting products

The following are the changes on C-2: Fixed Errata E153, E154, and Change of the 533 MHz system bus slew rate setting of ~1.9 V/ns to ~0.8 V/ns

Please refer to the Intel® Itanium® 2 Processor Specification Update for the latest specification information.

## Customer Impact of Change and Recommended Action:

It is recommended that application / system testing be completed on the new stepping. Testing should include verification that major operating systems continue to operate normally and run standard confidence tests.

## Products Affected / Intel Ordering Codes:

### Tray Dual Core Intel® Itanium® 2 Processor 9000 series

Processor Number	Product Code	Frequency	Pre-Conversion Production Units		Post-Conversion Production Units	
			S-Spec	MM #	S-Spec	MM #
9010	NE80549KE0256M	1.60 GHz 6M 533 FSB	S L9DJ	881319	S L9PA	883478
9015	NE80549KC017003	1.40 GHz 12M 400 FSB	S L9BW	880642	S L9PC	883486
9020	NE80549KEB17003	1.42 GHz 12M 533/400 FSB	S L9DE	881310	S L9PB	883479
9030	NE80549KE0258M	1.60 GHz 8M 533/400 FSB	S L9DH	881312	S L9P9	883477
9040	NE80549KE025009	1.60 GHz 18M 533/400 FSB	S L9DG	881305	S L9P8	883476
9050	NE80549KE025015	1.60 GHz 24M 533/400 FSB	S L9DF	881304	S L9PG	883474
9052	NE80549KE025LK	1.60 GHz 24M 533/400 FSB	S L98T	880556	S L9P7	883489

### Boxed Dual Core Intel® Itanium® 2 Processor 9000 series

Processor Number	Product Code	Frequency	Pre-Conversion Production Units		Post-Conversion Production Units	
			S-Spec	MM #	S-Spec	MM #
9010	BX805499010	1.60 GHz 6M 533 FSB	S L9DJ	883775	SL9PA	885382
9015	BX805499015	1.40 GHz 12M 400 FSB	S L9BW	883776	SL9PC	885383
9020	BX805499020	1.42 GHz 12M 533 FSB	S L9DE	883773	SL9PB	885381
9030	BX805499030	1.60 GHz 8M 533 FSB	S L9DH	883774	SL9P9	885380
9040	BX805499040	1.60 GHz 18M 533 FSB	S L9DG	883772	SL9P8	885379
9050	BX805499050	1.60 GHz 24M 533 FSB	S L9DF	883771	SL9PG	885374

## Reference Documents / Attachments:

Document:

Location #:

## PCN Revision History:

Date of Revision:

July 30, 2006

Revision Number:

00

Reason:

Originally Published PCN