



Product Change Notification

Change Notification #: PCN119206-00

Change Title: Minor Change,
Intel® Agilex™ Device Family,
PCN119206-00, Documentation,
Intel® Agilex™ Pin Connection Guidelines Update

Date of Publication: August 02, 2022

Key Characteristics of the Change:

Documentation

Forecasted Key Milestones:

Availability of Intel® Agilex™ PCG document Updates	Available now
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Description of Change to the Customer:

As communicated already via ADV2220, Intel is notifying customers of updates to the Intel® Agilex™ Device family Pin Connection Guidelines (PCG). Please note that there is no change to the design of the device.

Here is the link to the updated Agilex Pin Connection Guidelines (PCG) document:

<https://www.intel.com/content/www/us/en/docs/programmable/683112/current/device-family-pin-connection-guidelines.html>

Below table lists the updates in the Agilex Device family Pin Connection Guidelines.

These changes are also summarized in the revision history section within the PCG document.

<ul style="list-style-type: none">• Added a note on section 1.2.3 Optional/Dual-Purpose Configuration Pins. Refer to Page 9.
<ul style="list-style-type: none">• Updated the connection guidelines of the VCCIO_PIO[3][A,B,C,D,E,F] pins. Refer to Page 17 (Table 11)• Updated the connection guidelines of the REFCLK_GXP[L10A,L10C]_CH[0,2][p,n] pins. Refer to Page 35 (Table 17)• Updated the connection guidelines of the I_PIN_PERST_N_GXF pin. Refer to Page 40 (Table 19)• Updated the VCCL_HPS power supply voltage for the –4X and –4F speed grades in the connection guidelines of the VCCL_HPS pin. Refer to Page 44 (Table 22)• Updated the following tables to include details on the –4X speed grade device for the VCCL_HPS and VCCPLLDIG_HPS:<ul style="list-style-type: none">• Table: Power Supply Sharing Guidelines for Intel® Agilex™ Devices with P-Tile and E-Tile Transceivers. Refer to Page 61 (Table 35)• Table: Power Supply Sharing Guidelines for Intel® Agilex™ Devices with F-Tile and R-Tile Transceivers. Refer to Page 66 (Table 36)• Added note (5) in Figure: Example Power Supply Sharing Guidelines for Intel® Agilex™ Devices with P-Tile and E-Tile Transceivers. Refer to Page 65 (Figure 1)

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| <ul style="list-style-type: none"> Updated note (6) and added note (7) in Figure: Example Power Supply Sharing Guidelines for Intel® Agilex™ Devices with F-Tile and R-Tile Transceivers. Refer to Page 69 (Figure 2) |
| <ul style="list-style-type: none"> Updated the connection guidelines of the REFCLK_FGT[L,R]_Q[2,3]_CH[8,9]P and REFCLK_FGT[L,R]_Q[2,3]_CH[8,9]N pins. Refer to Page 39 (Table 19) |

Customer Impact of Change and Recommended Action:

Intel considers this a minor change. Refer to the revision history of the PCG documents for complete list and history of updates. Customers are requested to review the change and determine the impact on their designs.

Please see your local Intel representative for any questions or concerns regarding the change communicated in this notification.

Products Affected/Intel Ordering Codes:

The list of affected part numbers (OPNs) can be downloaded in Excel form:
<https://www.intel.com/content/dam/support/us/en/programmable/support-resources/bulk-container/pdfs/literature/pcn/adv2220-opn-list.xlsx>

PCN Revision History:

Date of Revision:	Revision Number:	Reason:
August 02, 2022	00	Originally Published PCN



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No response from customers will be deemed an acceptance of the change, and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below.

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