



Product Change Notification

109792 - 01

Information in this document is provided in connection with Intel products. No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document. Except as provided in Intel's Terms and Conditions of Sale for such products, Intel assumes no liability whatsoever, and Intel disclaims any express or implied warranty, relating to sale and/or use of Intel products including liability or warranties relating to fitness for a particular purpose, merchantability, or infringement of any patent, copyright or other intellectual property right. Intel products are not intended for use in medical, life saving, or life sustaining applications. Intel may make changes to specifications and product descriptions at any time, without notice.

Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

Americas Contact: <mailto:asmo.pcn@intel.com>

Asia Pacific Contact: <mailto:apacgccb@intel.com>

Europe Email: <mailto:eccb@intel.com>

Japan Email: <mailto:jccb.ijk@intel.com>

Copyright © Intel Corporation 2010. Other names and brands may be claimed as the property of others.

Celeron, Centrino, Intel, the Intel logo, Intel Core, Intel NetBurst, Intel NetMerge, Intel NetStructure, Intel SingleDriver, Intel SpeedStep, Intel StrataFlash, Intel Viiv, Intel XScale, Itanium, MMX, Paragon, PDCharm, Pentium, and Xeon are trademarks or registered trademarks of Intel Corporation or its subsidiaries in the United States and other countries.

Learn how to use Intel Trade Marks and Brands correctly at <http://www.intel.com/intel/legal/tmusage2.htm>.



Product Change Notification

Change Notification #: 109792 - 01
Change Title: DP55KG/DP55SB/DP55WG/DP55WB, PCN 109792-01, Product Design, Implemented B3 stepping of the PCH, Reason for Revision: Cancelled because the original date projected for depletion of current inventory has pushed out due to fluctuating supply and demand of the motherboards and chipset. A new PCN will be published when the B3 stepping of the PCH becomes available.
Date of Publication: March 15, 2010

Key Characteristics of the Change:

Product Design

Forecasted Key Milestones:

Date Customer Must be Ready to Receive Post-Conversion Material:	Feb 11, 2010
--	--------------

Description of Change to the Customer:

Reason for Revision: Cancelled because the original date projected for depletion of current inventory has pushed out due to fluctuating supply and demand of the motherboards and chipset. A new PCN will be published when the B3 stepping of the PCH becomes available.

Implemented B3 stepping of the PCH.

Customer Impact of Change and Recommended Action:

This change has been thoroughly evaluated to ensure that there are no quality, reliability, or functional implications to our customers. Customers may want to perform re-qualification, re-test, re-certification or standard validation testing for this change; see sample detail in PCN where applicable. This change is not expected to cause a change to the Microsoft Windows* operating system's software driver stack.

Products Affected / Intel Ordering Codes:

Product Code	MM#	Pre Change AA	Post Change AA
BLKDP55KG	901528	E47218-404	E47218-405
BOXDP55KG	901527	E47218-404	E47218-405
BLKDP55SB	901532	E47220-305	E47220-306
BOXDP55SB	901531	E47220-305	E47220-306
BLKDP55WG	901535	E57269-405	E57269-406
BOXDP55WG	901533	E57269-405	E57269-406
BLKDP55WB	902436	E64798-206	E64798-207
BOXDP55WB	902435	E64798-206	E64798-207

PCN Revision History:

Date of Revision:

January 15, 2010

March 15, 2010

Revision Number:

00

01

Reason:

Originally Published PCN

Cancelled because the original date projected for depletion of current inventory has pushed out due to fluctuating supply and demand of the motherboards and chipset. A new PCN will be published when the B3 stepping of the PCH becomes available.