



Product Change Notification

107176 - 00

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Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

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Product Change Notification

Change Notification #: 107176 - 00
Change Title: Boxed Intel® Core™ 2 Duo E6000 Sequence Processors and Dual-Core Intel® Xeon® Processor 3000 Series, PCN 107176-00, Product Design, E6300, E6400, 3040 and 3050 B2 to L2 Stepping Change & E6600, E6700, 3060 and 3070 S-Spec Change
Date of Publication: January 15, 2007

Key Characteristics of the Change:

Product Design

Forecasted Key Milestones:

Date Customer Must be Ready to Receive Post-Conversion Material:	Feb 09, 2007
Date of First Availability of Post-Conversion Material:	Feb 09, 2007

The date of "First Availability of Post-Conversion Material" is the projected date that a customer may expect to receive the Post-Conversion Materials. This date is determined by the projected depletion of inventory at the time of the PCN publication. The depletion of inventory may be impacted by fluctuating supply and demand, therefore, although customers should be prepared to receive the Post-Converted Materials on this date, Intel will continue to ship and customers may continue to receive the pre-converted materials until the inventory has been depleted.

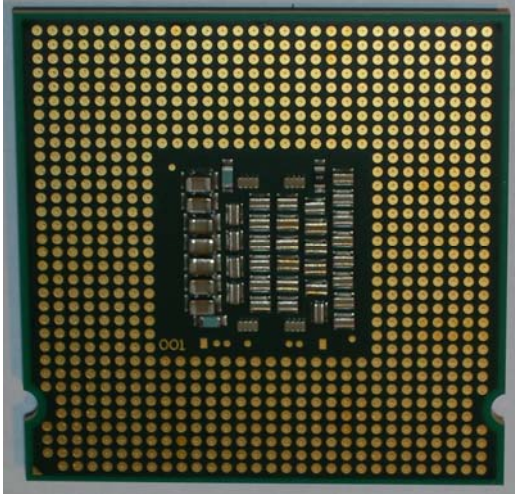
Description of Change to the Customer:

Intel® is initiating a B-2 to L-2 stepping conversion for the boxed Intel® Core™2 Duo processors E6300 and E6400 and the boxed Dual-Core Intel® Xeon® processors 3040 and 3050 where they will undergo the following changes:

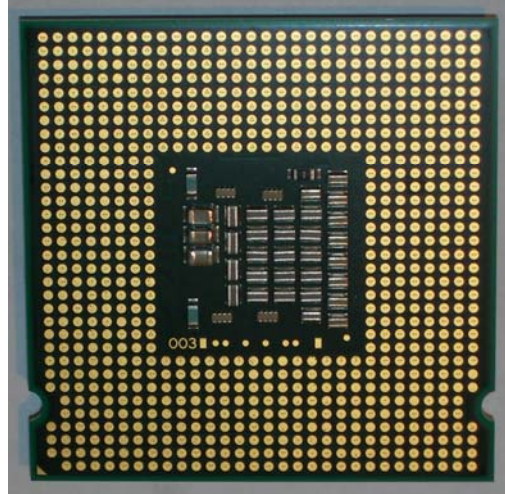
- New S-Spec and MM numbers for the converting products
- CPUID will change from 6F6 to 6F2
- Extended HALT power specification will reduce from 22 Watts to 12 Watts
- Die size optimized for manufacturability
- Minor visible difference between the B-2 and L-2 packages; see below for a visual comparison
- L-2 package is pin compatible with B-2 package

Visual B-2 & L-2 Package Differences

Original B-2 Package



New L-2 Package



While the packages are visually different, the L-2 stepping package is pin compatible with the B-2 stepping package and Intel anticipates no changes to customer platforms designed to Intel guidelines.

Additionally, the Extended HALT power specification will reduce from 22 Watts to 12 Watts for the boxed Intel® Core™ 2 Duo processors E6600 and E6700 and the boxed Dual-Core Intel® Xeon® processors 3060 and 3070, and Intel is initiating new S-Spec and MM numbers to better identify these processors having the new specification.

- The stepping will not change (remains B-2) for these processors
- CPUID will not change (remains 6F6) for these processors
- Die size and package will not change for these processors

Customer Impact of Change and Recommended Action:

Intel anticipates no impact to customers.

Products Affected / Intel Ordering Codes:

Processor Number	Frequency	Product Code	Original Package Production Units		Post-Conversion Production Units	
			S-Spec	MM #	S-Spec	MM #
E6700	2.66G 4M 1066	BX50557E6700	SL9S7	884979	SL9ZF	887658
E6600	2.40G 4M 1066	BX80557E6600	SL9S8	884980	SL9ZL	887664
E6400	2.13G 2M 1066	BX80557E6400	SL9S9	884981	SL9T9	887657
E6300	1.86G 2M 1066	BX80557E6300	SL9SA	884982	SL9TA	887659
E6300	1.86G 2M 1066	BX80557E6300T2	SL9SA	884983	SL9TA	887660
3070	2.66G 4M 1066	BX805573070	SL9U2	885771	SL9ZC	887665
3060	2.40G 4M 1066	BX805573060	SL9TZ	885772	SL9ZH	887666
3050	2.13G 2M 1066	BX805573050	SL9TY	885773	SL9VS	887661
3040	1.86G 2M 1066	BX805573040	SL9TW	885774	SL9VT	887662

Reference Documents / Attachments:

Document:

Location #:

PCN Revision History:

Date of Revision:

January 15, 2007

Revision Number:

00

Reason:

Originally Published PCN