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Product Change Notification

Change Notification #: 118332 - 00

Change Title: Intel® Stratix® 10 devices,

PCN 118332-00, Product Design,

Intel® FPGA Power and Thermal Calculator

(PTC) Update

Date of Publication: June 17, 2021

Key Characteristics of the Change:

Product Design

Forecasted Key Milestones:

Availability of update	Now
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Description of Change to the Customer:

This is the same change described in ADV2120 issued on June 11, 2021.

Intel is notifying customers of an important update to the Intel® FPGA Power and Thermal Calculator (PTC) for Intel® Stratix®10 devices.

Table 1

Update Details	KDB Article
In the Intel® FPGA Power and Thermal Calculator (PTC) version	Why does the Intel®
20.3 and earlier, you may see underestimated M20K dynamic	FPGA Power and Thermal
power for Intel® Stratix® 10 devices.	Calculator (PTC) show low
Permanent fix implemented in the Intel FPGA Power and Thermal	power estimations for
Calculator (PTC) version 20.4.	M20Ks in Intel® Stratix®
Refer to KDB link for further details and recommended actions.	10 devices?

Customer Impact of Change and Recommended Action:

Customers are requested to review the changes and determine the impact on their designs. Refer to the relevant KDB link in Table 1.

For questions or support, please contact your local Sales representative, or submit a question or request at the My Intel support page, log-in via: https://www.intel.com/content/www/us/en/my-intel/fpga-sign-in.html

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Products Affected / Intel Ordering Codes:

All Intel Stratix 10 Devices.

The list of affected part numbers (OPNs) can be downloaded in Excel form:

 $\underline{https://www.intel.com/content/dam/www/programmable/us/en/pdfs/literature/pcn/adv2120-opn-list.xlsx}$

PCN Revision History:

Date of Revision: Revision Number: Reason:

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Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

Americas Contact: asmo.pcn@intel.com
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