Product Change Notification

Change Notification #: 114674 - 00
Change Title: Intel® Pentium® N4200 Processor, Intel® Celeron® N3350 & N3450 Processors, PCN 114674-00, Product Design, B-0 to B-1 Stepping Conversion, New MM#s and S-specs to Order

Date of Publication: June 20, 2016

Key Characteristics of the Change:
Product Design

Forecasted Key Milestones:

<table>
<thead>
<tr>
<th>Forecasted Key Milestone</th>
<th>Date</th>
</tr>
</thead>
<tbody>
<tr>
<td>Last Product Discontinuance Order Date:</td>
<td>July 15, 2016</td>
</tr>
<tr>
<td>Last Product Discontinuance Shipment Date:</td>
<td>July 29, 2016</td>
</tr>
<tr>
<td>Date of samples availability:</td>
<td>N/A</td>
</tr>
<tr>
<td>Date of qualification data availability:</td>
<td>N/A</td>
</tr>
<tr>
<td>Date customer must be ready to receive post-conversion material:</td>
<td>July 29, 2016</td>
</tr>
<tr>
<td>Date of first availability of post-conversion material:</td>
<td>July 29, 2016</td>
</tr>
</tbody>
</table>

The date of "First Availability of Post-Conversion Material" is the projected date that a customer may expect to receive the Post-Conversion Materials. This date is determined by the projected depletion of inventory at the time of the PCN publication. The depletion of inventory may be impacted by fluctuating supply and demand, therefore, although customers should be prepared to receive the Post-Converted Materials on this date, Intel will continue to ship and customers may continue to receive the pre-converted materials until the inventory has been depleted.

Description of Change to the Customer:
In order to ensure a continuous supply of the Intel® Pentium® N4200 Processor and Intel® Celeron® N3350 & N3450 Processors listed in the Products Affected/Intel Ordering Codes table below, Intel will be initiating a B-0 to B-1 stepping conversion.

Description of changes between B-0 and B-1 stepping:
1. USB2 phy changed from using VNN powergood signals to VNN Always On powergood signals. (USB2 phy was already on the VNN Always On power rails, so the powergood indication was all that changed.)
2. REVID changed value from 0xA to 0xB
3. METAL_STEPING_ID changed value from 2'b01 to 2'b10

New post-conversion material master numbers (MM#s) and S-specs will be replacing the pre-conversion product data listed in the Products Affected/Intel Ordering Codes table. Pre-conversion product data will not be available to customers after the Last Product Discontinuance Order Date listed in this Product Change Notification.
Customer Impact of Change and Recommended Action:

Intel recommends the following B-1 validation:

1. Re-run a small representative subset of your S3/S4/ (S0ix if applicable) cycle testing to ensure your cycling environment continues to run as no-harm (smoke test, bat test, etc.).
2. Issue a wake event (key press, mouse click, etc.) from S3/S4/ (S0ix if applicable) from each USB2 device in your standard configurations for this product to ensure they wake the SoC and continue to function properly.
3. If your BIOS is impacted by the REVID change, you should validate those BIOS changes.

(The METAL_STEPPING_ID change is not visible to customers, and requires no further action from customers.)

Note: If your platform has been USB certified with B-0, then you do not need to get additional platform certification when you switch to B-1.

Intel anticipates no change or impact to customer platforms designed to Intel guidelines.

Customers should be ready to receive the new material by the "Date Customer Must be Ready to Receive New Material" as shown above in the milestones above.

Products Affected / Intel Ordering Codes:

<table>
<thead>
<tr>
<th>Pre-Conversion Product Data</th>
<th>Post-Conversion Product Data</th>
</tr>
</thead>
<tbody>
<tr>
<td>Product Code</td>
<td>Processor #</td>
</tr>
<tr>
<td>FH8066802979703</td>
<td>N4200</td>
</tr>
<tr>
<td>FH8066802979803</td>
<td>N3450</td>
</tr>
<tr>
<td>FH8066802980002</td>
<td>N3350</td>
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PCN Revision History:

<table>
<thead>
<tr>
<th>Date of Revision:</th>
<th>Revision Number:</th>
<th>Reason:</th>
</tr>
</thead>
<tbody>
<tr>
<td>June 20, 2016</td>
<td>00</td>
<td>Originally Published PCN</td>
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Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

Americas Contact: asmo.pcn@intel.com
Asia Pacific/PRC Contact: apacgccb@intel.com
Europe Email: eecb@intel.com
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