



Product Change Notification

108050 - 00

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Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

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Product Change Notification

Change Notification #: 108050 - 00
Change Title: Intel® Core™2 Duo Desktop Processors E6400 and E4300, PCN 108050-00, Product Design, L-2 to M-0 Stepping Conversion for Embedded Customer Application
Date of Publication: November 12, 2007

Key Characteristics of the Change:

Product Design

Forecasted Key Milestones:

Date of Samples Availability:	Nov 07, 2007
Date of Qualification Data Availability:	Nov 07, 2007
Date Customer Must be Ready to Receive Post-Conversion Material:	Jan 04, 2008
Date of First Availability of Post-Conversion Material:	Jan 04, 2008

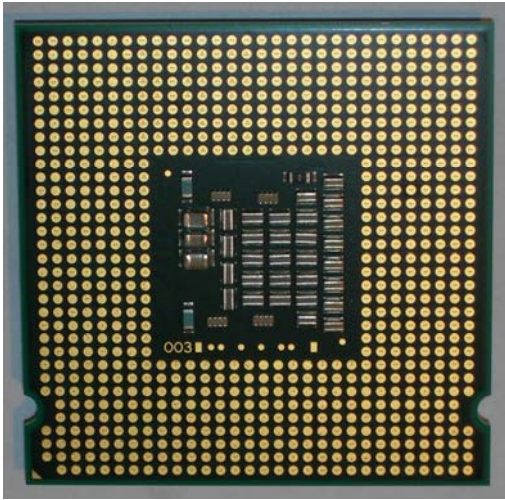
The date of "First Availability of Post-Conversion Material" is the projected date that a customer may expect to receive the Post-Conversion Materials. This date is determined by the projected depletion of inventory at the time of the PCN publication. The depletion of inventory may be impacted by fluctuating supply and demand, therefore, although customers should be prepared to receive the Post-Converted Materials on this date, Intel will continue to ship and customers may continue to receive the pre-converted materials until the inventory has been depleted.

Description of Change to the Customer:

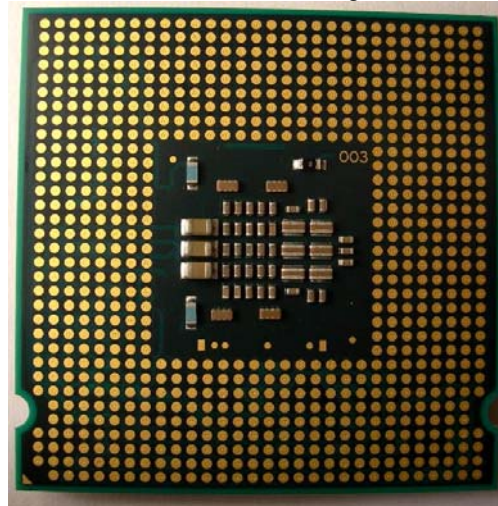
The Intel® Core™2 Duo Desktop processors E6400 and E4300 will undergo the following changes for the L-2 to M-0 stepping conversion:

- New S-Spec and Material Master Number for the converting products
- CPUID will change from 6F2 to 6FD
- The Electrical and Mechanical Specifications remain within the current specifications. Intel anticipates no changes to customer platforms designed to previous Intel guidelines
- Thermal specification has changed:
 - Extended HALT power specification will reduce from 12 Watts to 8 Watts
 - Maximum allowed Case Temperature will increase from 61.4°C to 73.2°C
 - Tcontrol offset will remain the same relative to 0 with the increase in maximum allowed Case Temperature which will help reduce acoustics
- Minor visible difference between the L-2 and M-0 packages (see below for a visual comparison).
 - While the packages are visually different, the M-0 package is pin-compatible with the L-2 package and Intel anticipates no changes to customer platforms designed to Intel guidelines

Original L-2 Package



New M-0 Package



Customer Impact of Change and Recommended Action:

Minimal re-qualification and/or validation is expected for the M-0 stepping conversion due to no feature set changes between the L-2 and M-0 steppings. Thermal qualification may be required if a new thermal solution is implemented to take advantage of the maximum allowed Case Temperature change.

Refer to Intel® Core™2 Duo Desktop Processor E6000 and E4000 Sequence Electrical, Mechanical, and Thermal Specifications (EMTS).

The Intel® Core™2 Duo desktop processors E6400 and E4300 M-0 stepping will require a BIOS update. Once customers implement the BIOS update, they will be able to accept both L-2 stepping material and M-0 stepping material. Customers should be ready to receive a combination of both L-2 stepping material and M-0 stepping material by the "Date Customer Must be Ready to Receive Post-Conversion Material" above.

Products Affected / Intel Ordering Codes:

Processor#	Frequency	Pre Conversion Product Code	Pre Conversion S-Spec	Pre Conversion MM#	Post Conversion Qualification Samples QDF#	Post Conversion Qualification Samples MM#	Post Conversion Product Code	Post Conversion S-Spec	Post Conversion MM#
E6400	2.13 GHz/ 1066/2M	HH80557PH0462M	S L9T9	885032	QWJQ	887101	HH80557PH0462M	SLA97	890246
E4300	1.80 GHz/ 800/2M	HH80557PG0332M	S L9TB	885034	QYRB	890036	HH80557PG0332M	SLA99	890247

Reference Documents / Attachments:

Document:

Location #:

PCN Revision History:

Date of Revision:

November 12, 2007

Revision Number:

00

Reason:

Originally Published PCN