



Product Change Notification

Change Notification #: 118193 - 00
Change Title: Intel® Stratix® 10 H-Tile/L-Tile Devices,
PCN 118193-00, Software,
PCIe Update
Date of Publication: April 7, 2021

Key Characteristics of the Change:

Software

Forecasted Key Milestones:

Milestone	Availability
Availability of PCIe Express* update to the Intel® Stratix® 10 H-Tile/L-Tile devices	Now

Description of Change to the Customer:

This is the same change described in ADV2105 issued on March 31, 2021.

Intel is notifying customers of an important PCIe Express* update to the Intel® Stratix® 10 H-Tile/L-Tile devices. Refer to Table 1 for details and link to KDB article with recommendations and further actions.

Table 1

Update Details	Impacted software versions	KDB Article
<ul style="list-style-type: none">When using the Intel® L-tile and H-tile Avalon® Streaming and Avalon® Memory Mapped IP for PCI Express* in Gen3 Root Port mode, correctable errors or link down training may be observed due to sub-optimal preset bit settings for PCIe* Upstream Port (USP)/Downstream Port (DSP) Gen3 Root Port IP on both H tile and L tile.Permanent fix implemented in Intel® Quartus® Prime Pro Edition software ver20.3 and above.	All Intel Quartus Prime Pro Edition software ver20.2 and prior	Why does the Intel® L-tile and H-tile Avalon® Streaming and Avalon® Memory Mapped IP for PCI Express* observe correctable errors/link down train when operating in Gen3 Root Port mode?

Customer Impact of Change and Recommended Action:

For more information, please contact your local Sales representative, or submit a question or request at the My Intel support page, log-in via:

<https://www.intel.com/content/www/us/en/my-intel/fpga-sign-in.html>

Products Affected / Intel Ordering Codes:

Impacted device families are Intel Stratix 10 H-Tile/L-Tile GX, MX, NX, SX, TX devices.

The list of affected part numbers (OPNs) can be downloaded in Excel form:

<https://www.intel.com/content/dam/www/programmable/us/en/pdfs/literature/pcn/adv2105-opn-list.xlsx>

PCN Revision History:

Date of Revision:	Revision Number:	Reason:
April 7, 2021	00	Originally Published PCN



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Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

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