



Product Change Notification

109194 - 00

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Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

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Product Change Notification

Change Notification #: 109194 - 00

Change Title: Tray Intel® Core™ i7-920 Processor, PCN 109194 -00, Product Design, C-0 to D-0 Stepping Conversion

Date of Publication: January 29, 2009

Key Characteristics of the Change:

Product Design

Forecasted Key Milestones:

Date of Samples Availability:	March 9-13, 2009
Date of Qualification Data Availability:	February 4, 2009
Date Customer Must be Ready to Receive Post-Conversion Material:	April 30, 2009
Date of First Availability of Post-Conversion Material:	April 10, 2009

The date of "First Availability of Post-Conversion Material" is the projected date that a customer may expect to receive the Post-Conversion Materials. This date is determined by the projected depletion of inventory at the time of the PCN publication. The depletion of inventory may be impacted by fluctuating supply and demand, therefore, although customers should be prepared to receive the Post-Converted Materials on this date, Intel will continue to ship and customers may continue to receive the pre-converted materials until the inventory has been depleted.

Description of Change to the Customer:

Intel is initiating a C-0 to D-0 stepping conversion for the Intel® Core™ i7-920 processor where it will undergo the following changes:

- New S-spec and Material Master numbers for the converting products.
- CPUID will change from 0x000106A4 to 0x000106A5.
- The electrical, mechanical and thermal specifications remain within the current specifications. Intel anticipates no changes to customer platforms designed to previous Intel guidelines.
- Readable serial number will be removed from the package ink swatch to fully mitigate the potential risk of the IHS cosmetically overlapping a marked serial number. There is no change to the ULT matrix content which continues to contain the serial number.

Original C-0 Ink Swatch



New D-0 Ink Swatch



Customer Impact of Change and Recommended Action:

Intel anticipates no change to customer platforms designed to Intel guidelines. Please refer to the Intel® Core™ i7 Processor Extreme Edition and Intel® Core™ i7 Processor Electrical, Mechanical, and Thermal Specifications (EMTS) for information on processor specifications and features.

Minimal re-qualification and/or validation is expected for the D-0 stepping conversion due to no feature set and/or specification changes between the C-0 and D-0 steppings.

The Intel® Core™ i7-920 processor on D-0 stepping will require a BIOS update due to the new CPUID. Once customers implement the BIOS update, they will be able to accept both C-0 and D-0 stepping material. Customers should be ready to receive a combination of both C-0 and D-0 stepping material by the “Date Customer Must be Ready to Receive Post-Conversion Material” above. For customers implementing their own BIOS, it is necessary to incorporate the latest Microcode Update (MCU) to support the D-0 stepping. Please contact your local Intel representative for further technical questions.

Products Affected / Intel Ordering Codes:

Intel® Core™ i7-920 Processor

Processor #	GHz	Pre Conversion				Qualification Samples		Post Conversion			
		Product Code	S-Spec	MM#	Stepping	QDF#	MM#	Product Code	S-Spec	MM#	Stepping
i7-920	2.66	AT80601000741AA	SLBCH	900054	C-0	Q1H7	900625	AT80601000741AA	SLBEJ	900817	D-0

Reference Documents / Attachments:

Document:

Location #:

PCN Revision History:

Date of Revision:

January 30, 2009

Revision Number:

00

Reason:

Originally Published PCN