



# Product Change Notification

**Change Notification #:** 118206 - 01  
**Change Title:** Intel® Arria® 10 and Intel® Stratix® 10 Devices, PCN 118206-01, Product Design, Advisory: Transceiver Updates  
**Reason for Revision:** Updated the description of change to add more clarity  
**Date of Publication:** April 21, 2021

## Key Characteristics of the Change:

Documentation, Product Design

## Forecasted Key Milestones:

<b>Date Customer Must be Ready to Receive Post-Conversion Material:</b>	Now
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## Description of Change to the Customer:

**Reason for Revision:** Updated the description of change to add more clarity.

In conjunction with ADV2116 previously communicated on 4/9/2021, Intel wants to ensure customers are notified of important updates related to the transceivers of Intel® Arria® 10, Intel® Stratix® 10 L/H-tile, and Intel® Stratix® 10 E-tile devices.

It was determined that transceivers are not preserved in performance when the device **is powered but** remains unprogrammed for an extended period of time.

### Recommended actions to preserve performance of the transceivers of a powered FPGA:

- Program the FPGA as soon as practically possible and ensure that unused channels to be used later are protected using the QSF assignment PRESERVE\_UNUSED\_XCVR\_CHANNEL.

Or

- If the FPGA is unprogrammed, ensure that it is only for a cumulative period less than 1 year at a junction temperature less than 60C.

This update is also described in the following Knowledge Base (KDB) article:

[https://www.intel.com/content/altera-www/global/en\\_us/index/support/support-resources/knowledge-base/hsio/2021/transceivers-are-not-preserved-when-the-device-remains-unprogram.html](https://www.intel.com/content/altera-www/global/en_us/index/support/support-resources/knowledge-base/hsio/2021/transceivers-are-not-preserved-when-the-device-remains-unprogram.html)

## **Customer Impact of Change and Recommended Action:**

This an advisory only; no change to the product at all. No change to form, fit, function, quality nor reliability.

For more information, please contact your local Sales representative, or submit a question or request at the My Intel support page, log-in via:

<https://www.intel.com/content/www/us/en/my-intel/fpga-sign-in.html>

## Products Affected / Intel Ordering Codes:

Intel® Arria® 10,  
Intel® Stratix® 10 L/H-tile  
Intel® Stratix® 10 E-tile

The list of affected part numbers (OPNs) can be downloaded in Excel form:

<https://www.intel.com/content/dam/www/programmable/us/en/pdfs/literature/pcn/adv2116-opn-list.xlsx>

## PCN Revision History:

Date of Revision:	Revision Number:	Reason:
April 14, 2021	00	Originally Published PCN
<a href="#">April 21, 2021</a>	01	<a href="#">Updated the description of change to add more clarity</a>



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**Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.**

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