Product Change Notification
109581 - 00

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Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

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Product Change Notification

Change Notification #: 109581 - 00
Change Title: Intel® 5500 & 5520 I/O Hubs (IOH) and Intel® X58 Express I/O Hub for Workstation & Server Products Only, PCN 109581-00, Product Design, B3 to C2 Design Change Conversion

Date of Publication: September 21, 2009

Key Characteristics of the Change:
Product Design

Forecasted Key Milestones:

| Date of Samples Availability: | Sept 11- Oct 23, 2009 |
| Date of Qualification Data Availability: | Oct 30, 2009 |
| Date of First Availability of Post-Conversion Material: | Oct 30, 2009 |
| Date Customer Must be Ready to Receive Post-Conversion Material: | Jan 22, 2010 |

The date of "First Availability of Post-Conversion Material" is the projected date that a customer may expect to receive the Post-Conversion Materials. This date is determined by the projected depletion of inventory at the time of the PCN publication. The depletion of inventory may be impacted by fluctuating supply and demand, therefore, although customers should be prepared to receive the Post-Converted Materials on this date, Intel will continue to ship and customers may continue to receive the pre-converted materials until the inventory has been depleted.

Description of Change to the Customer:
Intel is initiating a Design Change conversion for the B3 to C2 Intel® 5500 & 5520 I/O Hubs (IOH) and the Intel® X58 Express I/O Hub for Workstation and Server Products only, where they will undergo the following changes:

• Change only applies to Workstation and Server products,
• All High End Desktop Products will continue on the B3 stepping for production
• New S-spec and MM numbers for the converting products
• C-2 package is pin compatible with B-3 package
• No motherboard modifications required
• New Revision ID = 22h
Customer Impact of Change and Recommended Action:
This change applies to Workstation and Server products ONLY. All High End Desktop products making use of the Intel® X58 Express chipset will continue on the B3 stepping for production.

Please plan to validate the C-2 stepping of the Intel® 5500 & 5520 chipset and the Intel® X58 Express chipset. The new C-2 stepping of the IOH component of the Intel® 5500 & 5520 chipset and the Intel® X58 Express chipset is pin-to-pin compatible with the B-3 stepping and does not require any motherboard modifications. Design changes, however, have been made to the IOH component of the Intel® 5500 & 5520 chipset and the Intel® X58 Express chipset and will require customers to perform re-qualification. Please see the Intel® 5500 & 5520 chipset OR Intel® X58 Express chipset Specification Update and the Intel® 5500 & 5520 chipset OR Intel® X58 Express chipset BIOS Specification Update for any BIOS changes available for the C-2 stepping.

For technical details of the Intel® 5500 & 5520 chipset and the Intel® X58 Express chipset, please refer to the latest Specification Update available on CDI and/or intel.com product support web site.

Products Affected / Intel Ordering Codes:

<table>
<thead>
<tr>
<th>Marketing Name</th>
<th>Product Code</th>
<th>Pre Conversion</th>
<th>Post Conversion Qualification Samples</th>
<th>Post Conversion</th>
</tr>
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<tr>
<td></td>
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<td>Pre Conversion</td>
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<tr>
<td>Intel® 5500 I/O Hub (IOH)</td>
<td>AC5500</td>
<td>S LGMT</td>
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<td>S LGMU</td>
<td>901037</td>
<td>QMMN</td>
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<td>Intel® X58 Express I/O Hub (IOH)</td>
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<td>S LGMX</td>
<td>901076</td>
<td>QMML</td>
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</tbody>
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Reference Documents / Attachments:

Document: Location #:

PCN Revision History:

Date of Revision:   Revision Number:   Reason:
September 21, 2009   00   Originally Published PCN