



# Product Change Notification

## 108241 - 02

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Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

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# Product Change Notification

**Change Notification #:** 108241 - 02  
**Change Title:** Boxed Intel® Core™2 Extreme Processor QX9650, PCN 108241-02, Product Design, C-0 to C-1 Stepping Conversion, Reason for Revision: Adjust the Customer Ready to Receive Date  
**Date of Publication:** April 03, 2008

## Key Characteristics of the Change:

Product Design

## Forecasted Key Milestones:

<b>Date Customer Must be Ready to Receive Post-Conversion Material:</b>	Aug 04, 2008
<b>Date of First Availability of Post-Conversion Material:</b>	Aug 04, 2008

*The date of "First Availability of Post-Conversion Material" is the projected date that a customer may expect to receive the Post-Conversion Materials. This date is determined by the projected depletion of inventory at the time of the PCN publication. The depletion of inventory may be impacted by fluctuating supply and demand, therefore, although customers should be prepared to receive the Post-Converted Materials on this date, Intel will continue to ship and customers may continue to receive the pre-converted materials until the inventory has been depleted.*

## Description of Change to the Customer:

### Reason for Revision: Adjust the Customer Ready to Receive Date

The Boxed Intel® Core™2 Extreme Processor QX9650 will undergo the following changes for the C-0 to C-1 stepping conversion:

- New SSPEC and MM numbers for the converting product
- CPUID will change from: 0x00010676 to: 0x00010677
- C-0 package is pin compatible with C-1 package
- There are no changes to Electrical, Mechanical and Thermal processor Specifications.
- Intel anticipates no changes to customer platforms designed to Intel guidelines.

## Customer Impact of Change and Recommended Action:

There are no feature set changes between the C-0 and C-1 steppings.

The Intel® Core™2 Extreme Processor QX9650 C-1 stepping will require motherboard manufacturers to update their BIOS to support the new C-1 processor stepping. Minimal re-qualification and/or validation is expected for the stepping conversion. Processor engineering samples will be made available to channel motherboard manufacturers prior to general customer availability to facilitate BIOS updates and validation activities.

## Products Affected / Intel Ordering Codes:

Processor#	Frequency	Stepping	Pre Conversion Product Code	Pre Conversion S-Spec	Pre Conversion MM#	Post Conversion Product Code	Post Conversion S-Spec	Post Conversion MM#
QX9650	3 GHZ	C-0	BX80569QX9650	S LAN3	894493	BX80569QX9650	S LAWN	897412

## Reference Documents / Attachments:

Document:

Location #:

## PCN Revision History:

Date of Revision:

Revision Number:

Reason:

January 24, 2008

00

Originally Published PCN

February 6, 2008

01

Correct the post conversion MM number

April 3, 2008

02

Adjust the Customer Ready to Receive Date