



# **Product Change Notification**

## **113052 - 00**

**INFORMATION IN THIS DOCUMENT IS PROVIDED IN CONNECTION WITH INTEL PRODUCTS. NO LICENSE, EXPRESS OR IMPLIED, BY ESTOPPEL OR OTHERWISE, TO ANY INTELLECTUAL PROPERTY RIGHTS IS GRANTED BY THIS DOCUMENT. EXCEPT AS PROVIDED IN INTEL'S TERMS AND CONDITIONS OF SALE FOR SUCH PRODUCTS, INTEL ASSUMES NO LIABILITY WHATSOEVER AND INTEL DISCLAIMS ANY EXPRESS OR IMPLIED WARRANTY, RELATING TO SALE AND/OR USE OF INTEL PRODUCTS INCLUDING LIABILITY OR WARRANTIES RELATING TO FITNESS FOR A PARTICULAR PURPOSE, MERCHANTABILITY, OR INFRINGEMENT OF ANY PATENT, COPYRIGHT OR OTHER INTELLECTUAL PROPERTY RIGHT.**

**A "Mission Critical Application" is any application in which failure of the Intel Product could result, directly or indirectly, in personal injury or death. SHOULD YOU PURCHASE OR USE INTEL'S PRODUCTS FOR ANY SUCH MISSION CRITICAL APPLICATION, YOU SHALL INDEMNIFY AND HOLD INTEL AND ITS SUBSIDIARIES, SUBCONTRACTORS AND AFFILIATES, AND THE DIRECTORS, OFFICERS, AND EMPLOYEES OF EACH, HARMLESS AGAINST ALL CLAIMS COSTS, DAMAGES, AND EXPENSES AND REASONABLE ATTORNEYS' FEES ARISING OUT OF, DIRECTLY OR INDIRECTLY, ANY CLAIM OF PRODUCT LIABILITY, PERSONAL INJURY, OR DEATH ARISING IN ANY WAY OUT OF SUCH MISSION CRITICAL APPLICATION, WHETHER OR NOT INTEL OR ITS SUBCONTRACTOR WAS NEGLIGENT IN THE DESIGN, MANUFACTURE, OR WARNING OF THE INTEL PRODUCT OR ANY OF ITS PARTS.**

**Intel may make changes to specifications and product descriptions at any time, without notice. Designers must not rely on the absence or characteristics of any features or instructions marked "reserved" or "undefined". Intel reserves these for future definition and shall have no responsibility whatsoever for conflicts or incompatibilities arising from future changes to them. The information here is subject to change without notice. Do not finalize a design with this information.**

**The products described in this document may contain design defects or errors known as errata which may cause the product to deviate from published specifications. Current characterized errata are available on request.**

**Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.**

**Americas Contact:** [asmo.pcn@intel.com](mailto:asmo.pcn@intel.com)

**Asia Pacific/PRC Contact:** [apacgccb@intel.com](mailto:apacgccb@intel.com)

**Europe Email:** [eccb@intel.com](mailto:eccb@intel.com)

**Japan Email:** [jccb.ijkk@intel.com](mailto:jccb.ijkk@intel.com)

**Copyright © Intel Corporation 2014. Other names and brands may be claimed as the property of others.**

**BunnyPeople, Celeron, Celeron Inside, Centrino, Centrino Inside, Cilk, Core Inside, i960, Intel, the Intel logo, Intel AppUp, Intel Atom, Intel Atom Inside, Intel Core, Intel Inside, Intel Insider, the Intel Inside logo, Intel NetBurst, Intel NetMerge, Intel NetStructure, Intel SingleDriver, Intel SpeedStep, Intel Sponsors of Tomorrow, the Intel Sponsors of Tomorrow logo, Intel StrataFlash, Intel vPro, Intel XScale, InTru, the InTru logo, the InTru Inside logo, InTru soundmark, Itanium, Itanium Inside, MCS, MMX, Moblin, Pentium, Pentium Inside, Puma, skool, the skool logo, Sound Mark, The Creators Project, The Journey Inside, Thunderbolt, vPro Inside, VTune, Xeon, and Xeon Inside are trademarks of Intel Corporation in the U.S. and/or other countries.**

**Learn how to use Intel Trademarks and Brands correctly at <http://www.intel.com/intel/legal/tmusage2.htm>.**



# Product Change Notification

**Change Notification #:** 113052 - 00  
**Change Title:** Intel® Server Board BBS1200BTSR,  
DBS1200BTSR, S1200BTSR and  
S1200BTLRM  
PCN 113052-00, Product Design  
Update BIOS/ME/BMC Firmware  
**Date of Publication:** June 05, 2014

## Key Characteristics of the Change:

Product Design

## Forecasted Key Milestones:

<b>Date Customer Must be Ready to Receive Post-Conversion Material *:</b>	Jun 20, 2014
---	--------------

*\* Intel appreciates the customer's desire to receive the latest revision of products. However, Intel manages inventory on a first in first out (FIFO) basis at the MM# level. Subsequently, customer requests for a specific revision of material (below the MM#) will not be supported by Intel.*

## Description of Change to the Customer:

Intel is implementing the following changes on the Intel® Server Board BBS1200BTSR, DBS1200BTSR and S1200BTSR:

1. Update the BIOS from revision R02.00.0035 to R02.00.0041.
2. Update the Manageability Engine (ME) firmware from revision 2.02.00.036 to 2.02.00.049.

Intel is implementing the following changes on the Intel® Server Board S1200BTLRM:

1. Update the BIOS from revision R02.00.0038 to R02.00.0041.
2. Update the Integrated Baseboard Management Controller (BMC) firmware from revision R01.14r3503 to R01.17r5256.

The system software update package, including release notes for each, is available for downloading from <http://www.intel.com/support>. Please refer to the software release notes for a list of changes.

## Customer Impact of Change and Recommended Action:

Intel recommends that the customer perform a standard level of evaluation. Customers can download and evaluate the system software package at <http://www.intel.com/support>.

**Products Affected / Intel Ordering Codes:**

<b>Product Code</b>	<b>MM#</b>	<b>Pre Change TA</b>	<b>Pre Change PBA</b>	<b>Post Change TA</b>	<b>Post Change PBA</b>
BBS1200BTSR	920605	G27776-352	E98683-352	G27776-353	E98683-353
DBS1200BTSR	920607	G27774-352	E98683-352	G27774-353	E98683-353
S1200BTSR	920609	G21111-352	E98683-352	G21111-352	E98683-353
S1200BTLRM	923162	G71018-102	G68718-102	G71018-103	G68718-103

**PCN Revision History:****Date of Revision:**

June 5, 2014

**Revision Number:**

00

**Reason:**

Originally Published PCN