



# Product Change Notification

## 108984 - 01

Information in this document is provided in connection with Intel products. No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document. Except as provided in Intel's Terms and Conditions of Sale for such products, Intel assumes no liability whatsoever, and Intel disclaims any express or implied warranty, relating to sale and/or use of Intel products including liability or warranties relating to fitness for a particular purpose, merchantability, or infringement of any patent, copyright or other intellectual property right. Intel products are not intended for use in medical, life saving, or life sustaining applications. Intel may make changes to specifications and product descriptions at any time, without notice.

Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

**Americas Contact:** [asmo.pcn@intel.com](mailto:asmo.pcn@intel.com)

**Asia Pacific Contact:** [apacgccb@intel.com](mailto:apacgccb@intel.com)

**Europe Email:** [eccb@intel.com](mailto:eccb@intel.com)

**Japan Email:** [jccb.ijkk@intel.com](mailto:jccb.ijkk@intel.com)

Copyright © Intel Corporation 2008. Other names and brands may be claimed as the property of others.

Celeron, Centrino, Intel, the Intel logo, Intel Core, Intel NetBurst, Intel NetMerge, Intel NetStructure, Intel SingleDriver, Intel SpeedStep, Intel StrataFlash, Intel Viiv, Intel XScale, Itanium, MMX, Paragon, PDCharm, Pentium, and Xeon are trademarks or registered trademarks of Intel Corporation or its subsidiaries in the United States and other countries.

Learn how to use Intel Trade Marks and Brands correctly at <http://www.intel.com/intel/legal/tmusage2.htm>.



# Product Change Notification

**Change Notification #:** 108984 - 01  
**Change Title:** Intel(R) Server System SR2500, SR1550, SR1500 PCN 108984-01, Product Design, Update with New Bridge Board Connectors, Reason for Revision: cut-in roadmap change  
**Date of Publication:** December 03, 2008

## Key Characteristics of the Change:

Product Design

## Forecasted Key Milestones:

<b>Date Customer Must be Ready to Receive Post-Conversion Material:</b>	Dec 19, 2008
---	--------------

## Description of Change to the Customer:

### Reason for Revision: cut-in roadmap change

Update MidPlane and Backplane with new bridge board connectors. The new bridge board connectors are changed with additional socket depth. Change spares to use these new boards. New connectors are 100% backwards compatible with old boards.

## Customer Impact of Change and Recommended Action:

Customer to place future orders using new order code, see details in PCN.

## Products Affected / Intel Ordering Codes:

Pre Change Product Code	Pre Change MM#	Pre Change TA	Post Change Product Code	Post Change MM#	Post Change TA
FALPASMP	880520	D43781-002	FALPASMP	901687	D43781-003
FALSASMP	880521	D47628-004	FALSASMP	880521	D47628-005
ASR1500PASBP	894294	D47628-004	ASR1500PASBP	901834	D47628-005

## Reference Documents / Attachments:

**Document:** **Location #:**

## PCN Revision History:

Date of Revision:	Revision Number:	Reason:
November 11, 2008	00	Originally Published PCN
December 3, 2008	01	cut-in roadmap change

