



Product Change Notification

108047 - 00

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Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.

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Product Change Notification

Change Notification #: 108047 - 00
Change Title: Intel® Core™2 Duo processor LV L7300,
PCN 108047-00, Product Design, E-1 to G-0
Stepping Conversion
Date of Publication: November 06, 2007

Key Characteristics of the Change:

Product Design

Forecasted Key Milestones:

Date of Samples Availability:	Nov 27, 2007
Date of Qualification Data Availability:	Jan 22, 2008
Date Customer Must be Ready to Receive Post-Conversion Material:	Feb 05, 2008
Date of First Availability of Post-Conversion Material:	Dec 07, 2007

The date of "First Availability of Post-Conversion Material" is the projected date that a customer may expect to receive the Post-Conversion Materials. This date is determined by the projected depletion of inventory at the time of the PCN publication. The depletion of inventory may be impacted by fluctuating supply and demand, therefore, although customers should be prepared to receive the Post-Converted Materials on this date, Intel will continue to ship and customers may continue to receive the pre-converted materials until the inventory has been depleted.

Description of Change to the Customer:

The Intel® Core™2 Duo processor LV L7300 SKU on E-1 step will be converted to G-0 step in Q1'08. This processor is on the Santa Rosa Platform and is a Socket-P part.

- CPUID will change
- No change in terms of electrical, mechanical, and thermal specifications between E-1 step and G-0 step SKUs
- G-0 step incorporates speed path improvements to enable higher frequency bins

Contact your local Intel Corporation field representative for schedules to obtain latest specification information.

Customer Impact of Change and Recommended Action:

Minimal validation effort is expected for this Merom LV Socket-P conversion due to no feature changes between the Merom LV E-step and Merom LV G-Step processor.

- G-0 stepping will require a BIOS Update.
- New QDF#, S-Spec, MM numbers are provided below for G-0 Step Qualification Samples and Production units.

Products Affected / Intel Ordering Codes:

Processor#	Frequency	Pre Conversion Product Code	Pre Conversion S-Spec	Pre Conversion MM#	Post Conversion Qualification Samples QDF#	Post Conversion Qualification Samples MM#	Post Conversion Product Code	Post Conversion S-Spec	Post Conversion MM#
L7300	1.40 GHZ	LE80537LG0174M	S LA3S	888429	Q XMZ	888014	LE80537LG0174M	SLAEU	891455

Reference Documents / Attachments:

Document:

Location #:

PCN Revision History:

Date of Revision:

November 6, 2007

Revision Number:

00

Reason:

Originally Published PCN