



# Product Change Notification

**Change Notification #:** 116784 - 00  
**Change Title:** Intel® Stratix®V Devices, PCN 116784-00, Documentation, Data Sheet Update  
**Date of Publication:** February 26, 2019

## Key Characteristics of the Change:

Documentation

## Forecasted Key Milestones:

<b>Date Customer Must be Ready to Receive Post-Conversion Material:</b>	February 26, 2019
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## Description of Change to the Customer:

Intel is notifying customers of a documentation update for Stratix®V Devices. The announcement went out to customers already under ADV1833.

The minimum Data Hold time ( $t_{DH}$ ) timing specification for Active Serial (AS) configuration scheme has been updated.

**Table 1: Minimum  $t_{DH}$  Specification**

Speed Grade	Change From	Change To <sup>1</sup>
-1	0ns	2.9ns
-2	0ns	3.4ns
-3	0ns	3.7ns
-4	0ns	3.9ns

**Note 1: Applies to Commercial and Industrial grade**

The  $t_{DH}$  spec is stipulated in Table 53 of the datasheet. Please review the revision history for all other updates to the datasheet. The Stratix V device datasheet can be found here:

[https://www.intel.com/content/dam/www/programmable/us/en/pdfs/literature/hb/stratix-v/stx5\\_53001.pdf](https://www.intel.com/content/dam/www/programmable/us/en/pdfs/literature/hb/stratix-v/stx5_53001.pdf)

## Customer Impact of Change and Recommended Action:

Customers should review their board design against the updated Stratix V minimum  $t_{DH}$  spec, otherwise configuration may fail.

Customers can calculate the hold time required for Active Serial configuration scheme, as mentioned in the 'Evaluating Data Setup and Hold Timing Slack' section of AN822 to evaluate the hold time slack on the board. AN822 can be found here:

<https://www.intel.com/content/dam/www/programmable/us/en/pdfs/literature/an/an822.pdf>

For any additional questions, please contact your local Field Applications Engineer (FAE) or submit a Service Request at the [My Intel](#) support page.

## **Products Affected / Intel Ordering Codes:**

All Stratix V Devices

The list of affected OPNs can be downloaded in Excel form:

<https://www.intel.com/content/dam/www/programmable/us/en/pdfs/literature/pcn/adv1833-opn-list.xlsx>

## **PCN Revision History:**

<b>Date of Revision:</b>	<b>Revision Number:</b>	<b>Reason:</b>
February 26, 2019	00	Originally Published PCN



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**Should you have any issues with the timeline or content of this change, please contact the Intel Representative(s) for your geographic location listed below. No response from customers will be deemed as acceptance of the change and the change will be implemented pursuant to the key milestones set forth in this attached PCN.**

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